

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१८-२०१९		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
01.08.2018	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
02.08.2018	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
03.08.2018	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
04.08.2018	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
06.08.2018	1	Harvindra Kr. Srivastav	III	Operating System	About syllabus
07.08.2018				Class Teaching	
08.08.2018	3	Virendra Tiwari	I	Introduction	Generation of computer
09.08.2018	4	Virendra Tiwari	I	Introduction	Types of computer
10.08.2018	3	Virendra Tiwari	II	Digital circuits	NAND, NOR Logic gate
11.08.2018	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
12.08.2018	2	Harvindra Kr. Srivastav	III	Operating System overview	Computer System structure
13.08.2018				Class Teaching	
16.08.2018	5	Virendra Tiwari	I	Introduction	Major component of digital computer
17.08.2018	6	Virendra Tiwari	I	Introduction	Hardware
18.08.2018	5	Virendra Tiwari	II	Digital circuits	Universal Gates
20.08.2018	6	Virendra Tiwari	II	Digital circuits	Gates problems
21.08.2018	3	Harvindra Kr. Srivastav	III	Operating System overview	OS Structure
23.08.2018				Class Teaching	
24.08.2018	7	Virendra Tiwari	I	Introduction	Software, Firmware
25.08.2018	8	Virendra Tiwari	I	Introduction	Computer, Application
27.08.2018	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
28.08.2018	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
29.08.2018	4	Harvindra Kr. Srivastav	III	Operating system overview	OS functioned, facilities
30.08.2018				Class Teaching	
01.09.2018	9	Virendra Tiwari	I	Data representation	Introduction
04.09.2018	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
05.09.2018	9	Virendra Tiwari	II	Digital circuits	Demorgan Law in Boolean algebra
06.09.2018	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
07.09.2018	5	Harvindra Kr. Srivastav	III	Memory management	Introduction
08.09.2018				Class Teaching	

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10.09.2018	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
11.09.2018	12	Virendra Tiwari	I	Data Representation	Octal number system
12.09.2018	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
13.09.2018	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
14.09.2018	6	Harvindra Kr. Srivastav	III	Memory management	Single Contiguous allocations
15.09.2018				Class Teaching	
17.09.2018	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
18.09.2018	14	Virendra Tiwari	I	Data Representation	BCD number
19.09.2018	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
20.09.2018	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condition
22.09.2018	7	Harvindra Kr. Srivastav	III	Memory management	Partitioned allocation
24.09.2018				Monthly Evaluation	
25.09.2018	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
26.09.2018	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
27.09.2018	15	Virendra Tiwari	II	Combinational circuits	Introduction
28.09.2018	16	Virendra Tiwari	II	Combinational circuits	Half adder
01.10.2018	8	Harvindra Kr. Srivastav	III	Memory management	Relocation partitioned allocation
03.10.2018				Class Teaching	
04.10.2018	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
05.10.2018	18	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
06.10.2018	17	Virendra Tiwari	II	Combinational circuits	Full adder
09.10.2018	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
10.10.2018	9	Harvindra Kr. Srivastav	III	Memory management	Introduction paging
11.10.2018				Class Teaching	
12.10.2018	19	Virendra Tiwari	I	Introduction to programming	Flowchart, dataflow diagram
13.10.2018	20	Virendra Tiwari	I	Programming in C	An overview of C
15.10.2018	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
16.10.2018	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
17.10.2018	10	Harvindra Kr. Srivastav	III	Memory management	Example or problems on paging
22.10.2018				Class Teaching	

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24.10.2018	21	Virendra Tiwari	I	Programming in C	Structure of C program
25.10.2018	22	Virendra Tiwari	I	Programming in C	Storage class specifier
26.10.2018	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
27.10.2018	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
29.10.2018	11	Harvindra Kr. Srivastav	III	Memory management	Segmentation, demand page allocation
31.10.2018				Class Teaching	
01.11.2018	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
02.11.2018	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
03.11.2018	23	Virendra Tiwari	II	Combinational circuit	Edge-triggeredd flip-flop
12.11.2018	24	Virendra Tiwari	II	Combinational circuit	Encoders
13.11.2018	12	Harvindra Kr. Srivastav	III	Processor management	State models, case study of DOS
14.11.2018				Monthly Evaluation	
15.11.2018	25	Virendra Tiwari	I	Control Statement	True & false in C
16.11.2018	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
17.11.2018	25	Virendra Tiwari	II	Cobinational circuits	Decoders
18.11.2018	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
20.11.2018	13	Harvindra Kr. Srivastav	III	Processor management	Job schedulers and its type, Windows
					Operating System
21.11.2018				Class Teaching	
22.11.2018	27	Virendra Tiwari	I	Control statements	For statement, while statement
23.11.2018	28	Virendra Tiwari	I	Control statements	do-while & break
24.11.2018	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
26.11.2018	28	Virendra Tiwari	II	Combinational circuits	Shift Register
27.11.2018	14	Harvindra Kr. Srivastav	III	Processor Management	Process Scheduling
28.11.2018				Class Teaching	
29.11.2018	29	Virendra Tiwari	I	Control Statement	Contimute exit ()& goto
30.11.2018	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
01.12.2018	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
03.12.2018	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
06.12.2018	15	Harvindra Kr. Srivastav	III	Processor management	Job and process synchronisation

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07.12.2018				Class Teaching	
08.12.2018	31	Virendra Tiwari	I	Basic I/O	Concept of function
11.12.2018	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
12.12.2018	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
13.12.2018	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
14.12.2018	16	Harvindra Kr. Srivastav	III	Processor Management	Race conditions, Scheduling algorithm &
					its related problems
15.12.2018				Monthly Evaluation	
17.12.2018	33	Virendra Tiwari	I	Basic I/O	Recursion
18.12.2018	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
19.12.2018	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
20.1.2019	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
21.12.2018	17	Harvindra Kr. Srivastav	III	Processor management	Introduction dead lock, Dead lock detection
					Recovery & Prevention
22.12.2018				Class Teaching	
31.12.2018	35	Virendra Tiwari	I	Array	Introduction
01.01.2019	36	Virendra Tiwari	I	Array	One dimensional
02.01.2019	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
03.01.2019	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
04.01.2019	18	Harvindra Kr. Srivastav	III	Device Management	Introduction & its different technique
05.01.2019				Class Teaching	
07.01.2019	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
08.01.2019	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators,
					Pointer expressions, pointer & array
09.01.2019	37	Virendra Tiwari	II	Memory	Memory hierarchy, RAM, SRAM, DRAM
10.01.2019	38	Virendra Tiwari	II	Memory	Memory write ability & storage performance
11.01.2019	19	Harvindra Kr. Srivastav	III	Device management	I/O Device storage device, logical &
					physical, file system
12.01.2019				Class Teaching	
17.01.2019	39	Virendra Tiwari	I	Pointer	Strings & string library function

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