

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक					विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
01.08.2013	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
03.08.2013	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
05.08.2013	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
06.08.2013	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
07.08.2013	1	Sanjay Kumar Tiwari	III	Operating System	About syllabus
08.08.2013				Class Teaching	
10.08.2013	3	Virendra Tiwari	I	Introduction	Generation of computer
12.08.2013	4	Virendra Tiwari	I	Introduction	Types of computer
13.08.2013	3	Virendra Tiwari	II	Digital circuits	NAND, NOR Logic gate
14.08.2013	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
16.08.2013	2	Sanjay Kumar Tiwari	III	Operating System overview	Computer System structure
17.08.2013				Class Teaching	
19.08.2013	5	Virendra Tiwari	I	Introduction	Major component of digital computer
20.08.2013	6	Virendra Tiwari	I	Introduction	Hardware
22.08.2013	5	Virendra Tiwari	II	Digital circuits	Universal Gates
23.08.2013	6	Virendra Tiwari	II	Digital circuits	Gates problems
24.08.2013	3	Sanjay Kumar Tiwari	III	Operating System overview	OS Structure
26.08.2013				Class Teaching	
27.08.2013	7	Virendra Tiwari	I	Introduction	Software, Firmware
29.08.2013	8	Virendra Tiwari	I	Introduction	Computer, Application
30.08.2013	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
31.08.2013	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
02.09.2013	4	Sanjay Kumar Tiwari	III	Operating system overview	OS functioned, facilities
03.09.2013				Class Teaching/Evaluation	
04.09.2013	9	Virendra Tiwari	I	Data representation	Introduction
05.09.2013	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
06.09.2013	9	Virendra Tiwari	II	Digital circuits	Demorgan Law in Boolean algebra
07.09.2013	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
09.09.2013	5	Sanjay Kumar Tiwari	III	Memory management	Introduction
10.09.2013				Class Teaching	

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11.09.2013	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
12.09.2013	12	Virendra Tiwari	I	Data Representation	Octal number system
13.09.2013	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
14.09.2013	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
16.09.2013	6	Sanjay Kumar Tiwari	III	Memory management	Single Contiguous allocations
17.09.2013				Class Teaching	
20.09.2013	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
21.09.2013	14	Virendra Tiwari	I	Data Representation	BCD numbe
23.09.2013	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
24.09.2013	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condision
25.09.2013	7	Sanjay Kumar Tiwari	III	Memory management	Partioned allocation
26.09.2013				Class Teaching	
27.09.2013	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
28.09.2013	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
30.09.2013	15	Virendra Tiwari	II	Combinational circuits	Introduction
01.10.2013	16	Virendra Tiwari	II	Combinational circuits	Half adder
03.10.2013	8	Sanjay Kumar Tiwari	III	Memory management	Relocation partioned allocation
07.10.2013				Class Teaching/Evaluation	
08.10.2013	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
09.10.2013	18	Virendra Tiwari	I	Introduction to programming	Concept of alogrithm
10.10.2013	17	Virendra Tiwari	II	Combinational circuits	Full adder
11.10.2013	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
17.10.2013	9	Sanjay Kumar Tiwari	III	Memory management	Introduction paging
19.10.2013				Class Teaching	
21.10.2013	19	Virendra Tiwari	I	Introdution to programming	Flowchart, dataflow diagram
22.10.2013	20	Virendra Tiwari	I	Programming in C	An overview of C
23.10.2013	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
24.10.2013	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
25.10.2013	10	Sanjay Kumar Tiwari	III	Memory management	Example or problems on paging
26.10.2013				Class Teaching	

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28.10.2013	21	Virendra Tiwari	I	Programming in C	Structure of C program
29.10.2013	22	Virendra Tiwari	I	Programming in C	Storage class specifier
30.10.2013	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
06.11.2013	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
07.11.2013	11	Sanjay Kumar Tiwari	III	Memory management	Segmentation, demand page allocation
08.11.2013				Class Teaching	
09.11.2013	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
11.11.2013	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
12.11.2013	23	Virendra Tiwari	II	Combinational circuit	Edge-triggeredd flip-flop
15.11.2013	24	Virendra Tiwari	II	Combinational circuit	Encoders
16.11.2013	12	Sanjay Kumar Tiwari	III	Processor management	State models
18.11.2013				Evaluation	
19.11.2013	25	Virendra Tiwari	I	Control Statement	True & false in C
20.11.2013	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
21.11.2013	25	Virendra Tiwari	II	Cobinational circuits	Decoders
22.11.2013	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
23.11.2013	13	Sanjay Kumar Tiwari	III	Processor management	Job schedulers and its type
25.11.2013				Class Teaching	
26.11.2013	27	Virendra Tiwari	I	Control statements	For statement, while statement
27.11.2013	28	Virendra Tiwari	I	Control statements	do-while & break
28.11.2013	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
29.11.2013	28	Virendra Tiwari	II	Combinational circuits	Shift Register
30.11.2013	14	Sanjay Kumar Tiwari	III	Processor Management	Process Scheduling
02.12.2013				Class Teaching	
03.12.2013	29	Virendra Tiwari	I	Control Statement	Contimute exit ()& goto
06.12.2013	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
07.12.2013	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
09.12.2013	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
11.12.2013	15	Sanjay Kumar Tiwari	III	Processor management	Job and process synchronisation
12.12.2013				Class Teaching	

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13.12.2013	31	Virendra Tiwari	I	Basic I/O	Concept of function
14.12.2013	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
16.12.2013	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
17.12.2013	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
18.12.2013	16	Sanjay Kumar Tiwari	III	Processor Management	Race conditions
19.12.2013				Class Teaching/Evaluation	
20.12.2013	33	Virendra Tiwari	I	Basic I/O	Recursion
21.12.2013	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
23.12.2013	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
24.12.2013	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
26.12.2013	17	Sanjay Kumar Tiwari	III	Processor management	Scheduling algorithm & its related problems
27.12.2013				Class Teaching	
28.12.2013	35	Virendra Tiwari	I	Array	Introduction
30.12.2013	36	Virendra Tiwari	I	Array	One dimensional
31.12.2013	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
01.01.2014	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
02.01.2014	18	Sanjay Kumar Tiwari	III	Processor management	Introduction dead lock, Dead lock detection
04.01.2014				Class Teaching	
06.01.2014	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
07.01.2014	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators
08.01.2014	37	Virendra Tiwari	II	Memory	Memory hierachy
09.01.2014	38	Virendra Tiwari	II	Memory	Memory write a ability & storage performance
10.01.2014	19	Sanjay Kumar Tiwari	III	Processor management	Recovery & prevention
11.01.2014				Class Teaching	
13.01.2014	39	Virendra Tiwari	I	Pointer	Pointer expmressions, pointer & array
15.01.2014	40	Virendra Tiwari	I	Pointer	Strings & string library function
16.01.2014	39	Virendra Tiwari	II	Memory	Memory units
17.01.2014	40	Virendra Tiwari	II	Memory	Memory type-ROM, Masked programmed ROM
18.01.2014	20	Sanjay Kumar Tiwari	III	Device management	Introduction & its different technique
20.01.2014				Class Teaching/Evaluation	

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21.01.2014	41	Virendra Tiwari	I	Structure of unions	Concept of structure & unions
22.01.2014	42	Virendra Tiwari	I	Structure of unions	Structure & function array structure, structure pointer, Nested structure
23.01.2014	41	Virendra Tiwari	II	Memory	EPROM, EEPROM, flash memory
24.01.2014	42	Virendra Tiwari	II	Memory	RAM, SRAM, DRAM
25.01.2014	21	Sanjay Kumar Tiwari	III	Device management	I/O device, storage devices
27.01.2014				Class Teaching	
28.01.2014	43	Virendra Tiwari	I	File management	Defining & opening of a file
29.01.2014	44	Virendra Tiwari	I	File management	Closing a file
30.01.2014	43	Virendra Tiwari	II	Memory	Cache memory, virtual memory concept
31.01.2014	44	Virendra Tiwari	II	Memory	Synchronous & Enhanced Synchronous DRAM
01.02.2014	22	Sanjay Kumar Tiwari	III	Information management	File system, features of file system, general model of file.
03.02.2014				Class Teaching	
04.02.2014	45	Virendra Tiwari	I	File management	I/O operations
05.02.2014	23	Sanjay Tiwari	III	Information management	Logical & physical file system
06.02.2014	45	Virendra Tiwari	II	Multivibrators	Astable, monostable, bistable : Schmitt
07.02.2014	46	Virendra Tiwari	II	A/C converters	Digital to analog converter
08.02.2014	24	Sanjay Kumar Tiwari	III	Operating system	Case study of DOS
10.02.2014				Class Teaching	
11.02.2014	25	Sanjay Kumar Tiwari	III	Operating system	Window operating system
12.02.2014	47	Virendra Tiwari	II	Transistor	CMOS Logic, ECTL
13.02.2014	48	Virendra Tiwari	II	Transistor	Logic circuit, active & passive logic circuit, diode logic
14.02.2014	49	Virendra Tiwari	II	Transistor	RTL, PL, TTL