

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१६-२०२०		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
01.08.2019	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
02.08.2019	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
03.08.2019	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
06.08.2019	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
07.08.2019	1	Harvindra Kr. Srivastav	III	Operating System	About syllabus
08.08.2019				Class Teaching	
09.08.2019	3	Virendra Tiwari	I	Introduction	Generation of computer
10.08.2019	4	Virendra Tiwari	I	Introduction	Types of computer
13.08.2019	3	Virendra Tiwari	II	Digital circuits	NAND, NOR Logic gate
14.08.2019	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
16.08.2019	2	Harvindra Kr. Srivastav	III	Operating System overview	Computer System structure
17.08.2019				Class Teaching	
19.08.2019	5	Virendra Tiwari	I	Introduction	Major component of digital computer
20.08.2019	6	Virendra Tiwari	I	Introduction	Hardware
21.08.2019	5	Virendra Tiwari	II	Digital circuits	Universal Gates
22.08.2019	6	Virendra Tiwari	II	Digital circuits	Gates problems
24.08.2019	3	Harvindra Kr. Srivastav	III	Operating System overview	OS Structure
26.08.2019				Class Teaching	
27.08.2019	7	Virendra Tiwari	I	Introduction	Software, Firmware
28.08.2019	8	Virendra Tiwari	I	Introduction	Computer, Application
29.08.2019	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
30.08.2019	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
02.09.2019	4	Harvindra Kr. Srivastav	III	Operating system overview	OS functioned, facilities
03.09.2019				Class Teaching	
04.09.2019	9	Virendra Tiwari	I	Data representation	Introduction
05.09.2019	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
06.09.2019	9	Virendra Tiwari	II	Digital circuits	Demorgan Law in Boolean algebra
07.09.2019	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
09.09.2019	5	Harvindra Kr. Srivastav	III	Memory management	Introduction
11.09.2019				Class Teaching	

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13.09.2019	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
14.09.2019	12	Virendra Tiwari	I	Data Representation	Octal number system
16.09.2019	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
17.09.2019	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
18.09.2019	6	Harvindra Kr. Srivastav	III	Memory management	Single Contiguous allocations
20.09.2019				Class Teaching	
21.09.2019	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
23.09.2019	14	Virendra Tiwari	I	Data Representation	BCD number
24.09.2019	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
25.09.2019	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condition
26.09.2019	7	Harvindra Kr. Srivastav	III	Memory management	Partitioned allocation
27.09.2019				Monthly Evaluation	
30.09.2019	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
01.10.2019	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
03.10.2019	15	Virendra Tiwari	II	Combinational circuits	Introduction
04.10.2019	16	Virendra Tiwari	II	Combinational circuits	Half adder
05.10.2019	8	Harvindra Kr. Srivastav	III	Memory management	Relocation partitioned allocation
12.10.2019				Class Teaching	
14.10.2019	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
15.10.2019	18	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
16.10.2019	17	Virendra Tiwari	II	Combinational circuits	Full adder
17.10.2019	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
18.10.2019	9	Harvindra Kr. Srivastav	III	Memory management	Introduction paging
21.10.2019				Class Teaching	
22.10.2019	19	Virendra Tiwari	I	Introduction to programming	Flowchart, dataflow diagram
23.10.2019	20	Virendra Tiwari	I	Programming in C	An overview of C
30.10.2019	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
31.10.2019	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
01.11.2019	10	Harvindra Kr. Srivastav	III	Memory management	Example or problems on paging
04.11.2019				Class Teaching	

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05.11.2019	21	Virendra Tiwari	I	Programming in C	Structure of C program
06.11.2019	22	Virendra Tiwari	I	Programming in C	Storage class specifier
07.11.2019	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
09.11.2019	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
11.11.2019	11	Harvindra Kr. Srivastav	III	Memory management	Segmentation, demand page allocation
13.11.2019				Class Teaching	
14.11.2019	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
15.11.2019	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
16.11.2019	23	Virendra Tiwari	II	Combinational circuit	Edge-triggeredd flip-flop
18.11.2019	24	Virendra Tiwari	II	Combinational circuit	Encoders
19.11.2019	12	Harvindra Kr. Srivastav	III	Processor management	State models, case study of DOS
20.11.2019				Monthly Evaluation	
21.11.2019	25	Virendra Tiwari	I	Control Statement	True & false in C
22.11.2019	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
23.11.2019	25	Virendra Tiwari	II	Cobinational circuits	Decoders
25.11.2019	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
26.11.2019	13	Harvindra Kr. Srivastav	III	Processor management	Job schedulers and its type, Windows
					Operating System
27.11.2019				Class Teaching	
28.11.2019	27	Virendra Tiwari	I	Control statements	For statement, while statement
29.11.2019	28	Virendra Tiwari	I	Control statements	do-while & break
30.11.2019	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
02.12.2019	28	Virendra Tiwari	II	Combinational circuits	Shift Register
03.12.2019	14	Harvindra Kr. Srivastav	III	Processor Management	Process Scheduling
07.12.2019				Class Teaching	
09.12.2019	29	Virendra Tiwari	I	Control Statement	Contimute exit ()& goto
11.12.2019	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
12.12.2019	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
13.12.2019	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
14.12.2019	15	Harvindra Kr. Srivastav	III	Processor management	Job and process synchronisation

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16.12.2019				Class Teaching	
17.12.2019	31	Virendra Tiwari	I	Basic I/O	Concept of function
18.12.2019	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
19.12.2019	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
20.12.2019	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
21.12.2019	16	Harvindra Kr. Srivastav	III	Processor Management	Race conditions, Scheduling algorithm &
					its related problems
23.12.2019				Monthly Evaluation	
24.12.2019	33	Virendra Tiwari	I	Basic I/O	Recursion
26.12.2019	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
27.12.2019	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
28.12.2019	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
30.12.2019	17	Harvindra Kr. Srivastav	III	Processor management	Introduction dead lock, Dead lock detection
					Recovery & Prevention
31.12.2019				Class Teaching	
01.01.2020	35	Virendra Tiwari	I	Array	Introduction
02.01.2020	36	Virendra Tiwari	I	Array	One dimensional
03.01.2020	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
04.01.2020	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
06.01.2020	18	Harvindra Kr. Srivastav	III	Device Management	Introduction & its different technique
07.01.2020				Class Teaching	
08.01.2020	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
09.01.2020	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators,
					Pointer expressions, pointer & array
10.01.2020	37	Virendra Tiwari	II	Memory	Memory hierarchy, RAM, SRAM, DRAM
11.01.2020	38	Virendra Tiwari	II	Memory	Memory write ability & storage performance
13.01.2020	19	Harvindra Kr. Srivastav	III	Device management	I/O Device storage device, logical &
					physical, file system
16.01.2020				Class Teaching	
17.01.2020	39	Virendra Tiwari	I	Pointer	Strings & string library function

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