

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-तीन पाठ्यक्रम योजना : सत्र-2020-21 विषय : कम्प्यूटर साइंस					
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
16.07.20	1	Virendra Tiwari	I	Principle of comp. design	Basic components of computer system
17.07.20	2	Virendra Tiwari	I	Principle of Comp.design	Overview of Construction of ALU
18.07.20	1	Harvind Kr. Srivastav	III	Object Oriented System	Introduction to object oriented System
20.07.20	1	H.K.S	IV	Data Communication System	Transmission System
20.07.20	2	H.K.S	III	Object Oriented System	Overview of object oriented System
21.07.20	1	Virendra Tiwari	II	DBMS	Concept of Database and its evaluation
21.07.20	2	H.K.S	IV	Data Communication System	Purpose, Components Source Transmitter
22.07.20	-		-	CLASS TEACHING	
23.07.20	3	Virendra Tiwari	I	Principle Comp. Design	Overview of Operation of ALU
24.07.20	4	Virendra Tiwari	I	Register Transfer & Micro Operator	Register Transfer Language
27.07.20	3	H.K.S	III	OOPS	Abstract data types
27.07.20	3	H.K.S	IV	Data Communication System	Data Transmission: Frequency
28.07.20	4	H.K.S	III	OOPS	Inheritance
28.08.20	4	H.K.S	IV	Data Communication System	Spectrem & Band Width
29.07.20	2	Virendra Tiwari	II	DBMS	Data Abstraction
30.07.20	-		-	CLASS TEACHING	
30.07.20	5	Virendra Tiwari	I	Register Transfer & micro-Operater	Bus & Memory Transfer
04.08.20	6	Virendra Tiwari	I	Register Transfer & micro-Operater	Logic micro operators
04.08.20	5	H.K.S	IV	Data Communication System	Analog Data Tansmission
05.08.20	5	H.K.S	III	OOPS	Polymorphism
06.08.20	6	H.K.S	III	OOPS	Object Identity
07.08.20	3	Virendra Tiwari	II	DBMS	Data integration
08.08.20	-		-	CLASS TEACHING	
10.08.20	7	Virendra Tiwari	I	Register Transfer & micro-operator	Shift micro operators
10.08.20	6	H.K.S	IV	Data Communication System	Digital Data Transmission
11.08.20	8	Virendra Tiwari	I	Register Transfer & micro-operator	Bus Structure, Multiple bus Hierarchies
13.08.20	7	H.K.S	III	OOPS	Object modling concepts
14.08.20	8	H.K.S	III	OOPS	Object oriented proگرامing language
17.08.20	7	H.K.S	IV	Data Communication System	Data & Signals
17.08.20	4	Virendra Tiwari	II	DBMS	Components of DBM

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18.08.20	8	H.K.S	IV	Data & Signal	Analog & Digital Signaling of analog & Digital data	
18.08.20	-		-	CLASS TEACHING		
19.08.20	9	Virendra Tiwari	I	Register Transfer & micro-operator	Elements of Bus Design	
20.08.20	10	Virendra Tiwari	I	Register Transfer & Micro-Operator	PCI Bus Structure	
21.08.20	9	H.K.S	III	OOPS	Object oriented programing	
22.08.20	10	H.K.S	III	OOPS	Overview of C++ Programing	
24.08.20	9	H.K.S	IV	Data & Signal	Modem	
24.08.20	5	Virendra Tiwari	II	DBMS	Data & Models	
25.09.20	10	H.K.S	IV	Data & Signal	Modulation Techniques	
25.08.20	-		-	CLASS TEACHING		
26.08.20	11	Virendra Tiwari	I	Memory	Linear Structure Memory Organisation & Coders	
27.08.20	12	Virendra Tiwari	I	Memory`	Dimension of Memory Access	
28.08.20	11	H.K.S	III	C++ Programming	Support for data Obstraction	
01.09.20	11	H.K.S	IV	Data & Signal	CODEC, Digital Transmitter	
01.08.20	12	H.K.S	III	OOS	Support for object oriented programing	
02.08.20	6	Virendra Tiwari	II	DBMS	Three Level of Architure of DBMS	
03.08.20	-		-	CLASS TEACHING/TEST		
04.09.20	13	Virendra Tiwari	I	Memory	Connectivity Memory Chip	
05.09.20	14	Virendra Tiwari	I	Memory	Magnetic Disk	
07.09.20	12	H.K.S	IV	Data & signal	Transmission Impairments	
07.09.20	13	H.K.S	III	C++ Programming Langauage	Declaration & Constatnts	
08.09.20	13	H.K.S	IV	Data & signal	Attemation & Attemation distrortion	
08.09.20	14	H.K.S	III	C++ Programming Langauage	Function & File	
09.09.20	7	Virendra Tiwari	II	DBMS	Data Models Implementation	
10.09.20	-		-	CLASS TEACHING		
11.09.20	15	Virendra Tiwari	I	Memory	Connecting Memory Chip to a Computer	
12.09.20	16	Virendra Tiwari	I	Memory	Floopy Disk	
14.09.20	14	H.K.S	IV	Data & Signal	Delay Diotrortion, Noise	
14.09.20	15	H.K.S	III	C++ Programming Langauage	Linkages	
15.09.20	15	H.K.S	IV	Data & Signal	Introduction to Network	

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15.09.20	16	H.K.S	III	Class & Objects	Defination of Class
16.09.20	8	H.K.S	II	DBMS	Relational Data
18.09.20	-			CLASS TEACHING	
19.09.20	17	Virendra Tiwari	I	Memory	Magnetic Tape
21.09.20	16	Virendra Tiwari	IV	Data & Signal	TCP/IP Reference Method
21.09.20	18	Virendra Tiwari	I	I/O Device	Cassettes & Cartidge
22.09.20	17	H.K.S	IV	Tansmission Media	Meganetic Media
22.09.20	17	H.K.S	III	Class & Objects	Class Declaration
23.09.20	18	H.K.S	III	Class & Objects	Object Declaration
24.09.20	9	H.K.S	II	DBMS	Network Hierarchical
25.09.20	-		-	CLASS TEACHING/TEST	
26.09.20	19	Virendra Tiwari	I	I/O Device	Fundamentals of Keyboards, Printers
28.09.20	18	H.K.S	IV	Transmission Media	Twisted Pair Cable
28.09.20	20	Virendra Tiwari	I	I/O Device	CRT & LCD Displays Devices
29.09.20	19	H.K.S	IV	Transmission Media	Baseband & Broadband Coaxial Cables
29.09.20	19	H.K.S	III	Class & Objects	Class Function Defination Member
30.09.20	20	H.K.S	III	Class & Objects	Defination Outside the class, declartion,
					scope resulation oreater
01.10.20	10	Virendra Tiwari	II	DBMS	Relation & Manipulation
03.10.20	-		-	CLASS TEACHING/TEST	
05.10.20	21	Virendra Tiwari	I	CPU	General Register & Organisation
05.10.20	20	H.K.S	IV	Transmission Media	Fiber Optics
06.10.20	22	Virendra Tiwari	I	CPU	Memory Stack
06.10.20	21	H.K.S	IV	Wireless Transmission	Radio Transmission
07.10.20	21	H.K.S	III	Class & Object	Private & Public Member Functions
09.10.20	22	H.K.S	III	Clas & Object	Nesting member function creating objetcs
10.10.20	11	Virendra Tiwari	II	DBMS	Relational Algebra
12.10.20	-		-	CLASS TEACHING/TEST	
12.10.20	22	H.K.S	IV	Wireless Transmission	Microwave Transmission
13.10.20	23	Virendra Tiwari	I	CPU	One Address & Two Address Instructions

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13.10.20	24	H.K.S	IV	Wireless Transmission	ISDN, ATM
14.10.20	22	Virendra Tiwari	I	CPU	Data Transfer
15.10.20	23	H.K.S	III	Calsses & Objects	Accesssing Member Functions
16.10.20	23	H.K.S	III	Calsses & Objects	Accesssing Member Functions
17.10.20	11	Virendra Tiwari	II	DBMS	Relational Calculas SQL
19.10.20	25	H.K.S	IV	Wireless Transmission	Data Linklayer, Services, Framing
19.10.20				CLASS TEACHING	
20.10.20	23	Virendra Tiwari	I	CPU	Arithmetic Logic & Shift
20.10.20	26	H.K.S	IV	Wireless Transmission	Error control, error detecting & correcting codes
21.10.20	24	Virendra Tiwari	I	CPU	Softwere & Hardware Interrupt
22.10.20	25	Virendra Tiwari	II	Class & Objects	Array of Objects
23.10.20	26	Virendra Tiwari	II	Class & Objects	Objects as function Agreement
29.10.20	12	Virendra Tiwari	II	DBMS	Funtional Dependencies Ist & 3rd Normal form
31.10.20	-		-	CLASS TEACHING/TEST	
02.11.20	25	Virendra Tiwari	I	CPU	Arithmetic & Instruction Pipeline
02.11.20	27	H.K.S	IV	Wireless Transmission	Data links protocols
03.11.20	26	Virendra Tiwari	I	Control Unit	Construction of instruction
03.11.20	30	H.K.S	IV	HDLC	Static & dynamic channel allocation in Lans
04.11.20	27	H.K.S	III	Operator Overloading	Opertaor Function, User Defined Type conversion
05.11.20	28	H.K.S	III	Operator Overloading	Literal , Large Object
06.11.20	14	Virendra Tiwari	II	Relation Database Design	BCNF
07.11.20	-			CLASS TEACHING	
09.11.20	29	Virendra Tiwari	I	Control Unit	Intruccion Cycle Organisation
09.11.20	28	H.K.S	IV	HDLC	Dynamic channel allocation in Lans
10.11.20	30	Virendra Tiwari	I	Control Unit	Control Register
10.11.20	29	H.K.S	IV	Multiple Access Protocol	ALOHA, Congention Control algo
17.11.20	29	H.K.S	III	Operator Overloading	Assignment & Imitialisation Subscribing

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17.11.20	30	H.K.S	IV	Multiple access protocol	CSMA/CD, IEEE Standards 802.3
18.11.20	30	H.K.S	III	Operator Overloading	A String Class
19.11.20	15	Virendra Tiwari	II	FoxPro	Introduction of FoxPro
21.11.20	-			CLASS TEACHING	
23.11.20	31	Virendra Tiwari	I	Control unit	Execution Cycle Organisation of Control
					Registers
23.12.20	31	H.K.S	IV	MAP	Ethernet,802.4, Token Bus
24.11.20	32	Virendra Tiwari	I	Control unit	Sequence of Operation of Control Register
24.12.20	32	H.K.S	IV	MAP	Ethernet,802.4, Token ring, Bridges
26.11.20	31	H.K.S	III	Operator overloading	Inheritance through extending classes
27.11.20	32	H.K.S	III	Operator overloading	Inheritance through extending classes
28.11.20	16	Virendra Tiwari	II	FoxPro	Database Construction
01.12.20	33	H.K.S	IV	MAP	Routers, Gateways Routing algos
01.12.20	-			CLASS TEACHING	
02.12.20	33	Virenda Tiwari	I	Control unit	Branch
03.12.20	34	Virendr Tiwari	I	Control Unit	Arithmetic operators
07.12.20	34	H.K.S	IV	MAP	Internet Working
07.12.20	33	H.K.S	III	OOPS	Visibilty mode, Signal Inheritance
08.12.20	35	H.K.S	IV	MAP	IP Addressing
08.12.20	34	H.K.S	III	OOPS	Derived Class, Defining Drived Classes
09.12.20	17	Virendra Tiwari	II	Foxpro	Searching
11.12.20	-			CLASS TEACHING/TEST	
12.11.20	35	Virendr Tiwari	I	Control Unit	Skip or Jump Instruction
14.12.20	36	H.K.S	IV	MAP	IP Addressing Numericab
14.12.20	36	Viendra Tiwari	I	Control Unit	Register Language
15.12.20	37	H.K.S	IV	MAP	Revision
15.12.20	35	H.K.S	III	Streaming & Templets	C-I/O Formatting Files & Streams
16.12.20	36	H.K.S	III	Steaming Temples	C I/O Design of Liberties
17.12.20	18	Virendra Tiwari	II	Foxpro	Sorting
18.12.20	-			CLASS TEACHING	

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21.12.20	37	Viendra Tiwari	I	Control Unit	Micro-Programming
21.12.20	38	H.K.S	IV	MAP	Revision
22.12.20	38	Virendra Tiwari	I	Control Unit	Micro-Programming
22.12.20	39	H.K.S	IV	MAP	Problem
23.12.20	37	H.K.S	III	OOA & Design	Object Oriented Analysis System Design
24.12.20	38	H.K.S	III	OOA & Design	Object Design
26.12.20	19	Virendra Tiwari	II	Fox Pro	Indexing
28.12.20	40	H.K.S	IV	MAP	Revision
28.12.20	-			CLASS TEACHING	
29.12.20	39	Virendra Tiwari	I	Control Unit	Micro-Programming
29.01.20	41	H.K.S	IV	MAP	Revision
30.12.20	40	Virendra Tiwari	I	Control Unit	Program
31.12.20	39	H.K.S	III	OOA & Design	Semantic
01.01.21	40	Virendra Tiwari	III	OOA & Design	Entity, Relationship
02.01.21	20	Virendra Tiwari	II	Foxpro	Updation
04.01.21	-			CLASS TEACHING	
04.01.21	41	Virendra Tiwari	I	Control Unit	Shift Instruction
06.01.21	42	Virendra Tiwari	I	Control Unit	Revision
07.01.21	41	H.K.S	III	OOA & Design	Modeling
08.01.21	42	H.K.S	III	OOA & Design	Booch's
09.01.21	21	Virendra Tiwari	II	FoxPro	Screen, Designing, Programming
11.01.21	-			CLASS TEACHING	
11.01.21	42	H.K.S	IV	MAP	Revision
12.01.21	43	Virendra Tiwari	I	CPU	Revision
12.01.21	43	H.K.S	IV	Data & Signal	Revision
13.01.21	44	Virendra Tiwari	I	CPU	Revision
16.01.21	43	H.K.S	III	OOA & Design	Booch's ooa, ood approach
18.01.21	44	H.K.S	III	OOA & Design	Sementic Rules
18.01.21	44	H.K.S	IV	Data & Signal	Revision
19.01.21	22	Virendra Tiwari	II	DBMS	Revision

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