

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१६-२०१७		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
01.08.2016	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
02.08.2016	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
03.08.2016	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
04.08.2016	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
05.08.2016	1	Sanjay Kumar Tiwari	III	Operating System	About syllabus
08.08.2016				Class Teaching	
09.08.2016	3	Virendra Tiwari	I	Introduction	Generation of computer
10.08.2016	4	Virendra Tiwari	I	Introduction	Types of computer
11.08.2016	3	Virendra Tiwari	II	Digital circuits	NAND. NOR Logic gate
12.08.2016	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
13.08.2016	2	Sanjay Kumar Tiwari	III	Operating System overview	Computer System structure
15.08.2016				Class Teaching	
16.08.2016	5	Virendra Tiwari	I	Introduction	Major component of digital computer
17.08.2016	6	Virendra Tiwari	I	Introduction	Hardware
19.08.2016	5	Virendra Tiwari	II	Digital circuits	Universal Gates
20.08.2016	6	Virendra Tiwari	II	Digital circuits	Gates problems
22.08.2016	3	Sanjay Kumar Tiwari	III	Operating System overview	OS Structure
23.08.2016				Class Teaching	
24.08.2016	7	Virendra Tiwari	I	Introduction	Software, Firmware
26.08.2016	8	Virendra Tiwari	I	Introduction	Computer, Application
29.08.2016	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
30.08.2016	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
31.08.2016	4	Sanjay Kumar Tiwari	III	Operating system overview	OS functioned, facilities
01.09.2016				Class Teaching	
02.09.2016	9	Virendra Tiwari	I	Data representation	Introduction
03.09.2016	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
05.09.2016	9	Virendra Tiwari	II	Digital circuits	Demorgham Law in Boolean algebra
06.09.2016	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
07.09.2016	5	Sanjay Kumar Tiwari	III	Memory management	Introduction
08.09.2016				Class Teaching	

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१६-२०१७		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
09.09.2016	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
10.09.2016	12	Virendra Tiwari	I	Data Representation	Octal number system
14.09.2016	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
15.09.2016	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
16.09.2016	6	Sanjay Kumar Tiwari	III	Memory management	Single Contiguous allocations
18.09.2016				Class Teaching	
19.09.2016	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
21.09.2016	14	Virendra Tiwari	I	Data Representation	BCD number
22.09.2016	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
23.09.2016	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condition
24.09.2016	7	Sanjay Kumar Tiwari	III	Memory management	Partitioned allocation
26.09.2016				Monthly Evaluation	
27.09.2016	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
28.09.2016	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
29.09.2016	15	Virendra Tiwari	II	Combinational circuits	Introduction
03.10.2016	16	Virendra Tiwari	II	Combinational circuits	Half adder
04.10.2016	8	Sanjay Kumar Tiwari	III	Memory management	Relocation partitioned allocation
05.10.2016				Class Teaching	
06.10.2016	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
07.10.2016	18	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
08.10.2016	17	Virendra Tiwari	II	Combinational circuits	Full adder
14.10.2016	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
15.10.2016	9	Sanjay Kumar Tiwari	III	Memory management	Introduction paging
17.10.2016				Class Teaching	
18.10.2016	19	Virendra Tiwari	I	Introduction to programming	Flowchart, dataflow diagram
19.10.2016	20	Virendra Tiwari	I	Programming in C	An overview of C
20.10.2016	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
21.10.2016	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
22.10.2016	10	Sanjay Kumar Tiwari	III	Memory management	Example or problems on paging
24.10.2016				Class Teaching	

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१६-२०१७		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
25.10.2016	21	Virendra Tiwari	I	Programming in C	Structure of C program
26.10.2016	22	Virendra Tiwari	I	Programming in C	Storage class specifier
02.11.2016	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
03.11.2016	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
04.11.2016	11	Sanjay Kumar Tiwari	III	Memory management	Segmentation, demand page allocation
05.11.2016				Class Teaching	
08.11.2016	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
09.11.2016	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
10.11.2016	23	Virendra Tiwari	II	Combinational circuit	Edge-triggeredd flip-flop
11.11.2016	24	Virendra Tiwari	II	Combinational circuit	Encoders
12.11.2016	12	Sanjay Kumar Tiwari	III	Processor management	State models, case study of DOS
15.11.2016				Monthly Evaluation	
17.11.2016	25	Virendra Tiwari	I	Control Statement	True & false in C
18.11.2016	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
19.11.2016	25	Virendra Tiwari	II	Cobinational circuits	Decoders
21.11.2016	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
22.11.2016	13	Sanjay Kumar Tiwari	III	Processor management	Job schedulers and its type, Windows
					Operating System
23.11.2016				Class Teaching	
25.11.2016	27	Virendra Tiwari	I	Control statements	For statement, while statement
26.11.2016	28	Virendra Tiwari	I	Control statements	do-while & break
28.11.2016	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
29.11.2016	28	Virendra Tiwari	II	Combinational circuits	Shift Register
30.11.2016	14	Sanjay Kumar Tiwari	III	Processor Management	Process Scheduling
01.12.2016				Class Teaching	
02.12.2016	29	Virendra Tiwari	I	Control Statement	Contimute exit ()& goto
03.12.2016	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
06.12.2016	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
07.12.2016	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
08.12.2016	15	Sanjay Kumar Tiwari	III	Processor management	Job and process synchronisation

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१६-२०१७		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
09.12.2016				Class Teaching	
12.12.2016	31	Virendra Tiwari	I	Basic I/O	Concept of function
14.12.2016	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
15.12.2016	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
16.12.2016	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
17.12.2016	16	Sanjay Kumar Tiwari	III	Processor Management	Race conditions, Scheduling algorithm &
					its related problems
19.12.2016				Monthly Evaluation	
20.12.2016	33	Virendra Tiwari	I	Basic I/O	Recursion
21.12.2016	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
22.12.2016	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
24.12.2016	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
26.12.2016	17	Sanjay Kumar Tiwari	III	Processor management	Introduction dead lock, Dead lock detection
					Recovery & Prevention
27.12.2016				Class Teaching	
28.12.2016	35	Virendra Tiwari	I	Array	Introduction
29.12.2016	36	Virendra Tiwari	I	Array	One dimensional
30.12.2016	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
31.12.2016	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
02.01.2017	18	Sanjay Kumar Tiwari	III	Device Management	Introduction & its different technique
03.01.2017				Class Teaching	
04.01.2017	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
06.01.2017	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators,
					Pointer expressions, pointer & array
07.01.2017	37	Virendra Tiwari	II	Memory	Memory hierarchy, RAM, SRAM, DRAM
09.01.2017	38	Virendra Tiwari	II	Memory	Memory write ability & storage performance
10.01.2017	19	Sanjay Kumar Tiwari	III	Device management	I/O Device storage device, logical &
					physical, file system
11.01.2017				Class Teaching	
12.01.2017	39	Virendra Tiwari	I	Pointer	Strings & string library function

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक पाठ्यक्रम योजना : सत्र २०१६-२०१७ विषय : कम्प्यूटर साइंस

दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
13.01.2017	40	Virendra Tiwari	I	Structure of unions	Concepts, Structure & function array
					structure, structure pointer, Nested structure
16.01.2017	39	Virendra Tiwari	II	Memory	Memory units, EPROM, EEPROM,
					flash memory
17.01.2017	40	Virendra Tiwari	II	Memory	Memory type-ROM, Masked programmed ROM
					Cache memory, virtual memory concept
18.01.2017	20	Sanjay Kumar Tiwari	III	Information management	File system, case study of DOS & window
					operating system
19.01.2017				Monthly Evaluation	
20.01.2017	41	Virendra Tiwari	I	File management	Definitions, opening & closing of a file