

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१५-२०१६		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
01.08.2015	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
03.08.2015	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
04.08.2015	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
05.08.2015	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
06.08.2015	1	Sanjay Kumar Tiwari	III	Operating System	About syllabus
07.08.2015				Class Teaching	
08.08.2015	3	Virendra Tiwari	I	Introduction	Generation of computer
10.08.2015	4	Virendra Tiwari	I	Introduction	Types of computer
11.08.2015	3	Virendra Tiwari	II	Digital circuits	NAND. NOR Logic gate
12.08.2015	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
13.08.2015	2	Sanjay Kumar Tiwari	III	Operating System overview	Computer System structure
14.08.2015				Class Teaching	
17.08.2015	5	Virendra Tiwari	I	Introduction	Major component of digital computer
18.08.2015	6	Virendra Tiwari	I	Introduction	Hardware
20.08.2015	5	Virendra Tiwari	II	Digital circuits	Universal Gates
21.08.2015	6	Virendra Tiwari	II	Digital circuits	Gates problems
22.08.2015	3	Sanjay Kumar Tiwari	III	Operating System overview	OS Structure
23.08.2015				Class Teaching	
24.08.2015	7	Virendra Tiwari	I	Introduction	Software, Firmware
25.08.2015	8	Virendra Tiwari	I	Introduction	Computer, Application
26.08.2015	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
27.08.2015	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
31.08.2015	4	Sanjay Kumar Tiwari	III	Operating system overview	OS functioned, facilities
01.09.2015				Class Teaching	
03.09.2015	9	Virendra Tiwari	I	Data representation	Introduction
04.09.2015	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
07.09.2015	9	Virendra Tiwari	II	Digital circuits	Demorgham Law in Boolean algebra
08.09.2015	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
09.09.2015	5	Sanjay Kumar Tiwari	III	Memory management	Introduction
10.09.2015				Class Teaching	

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11.09.2015	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
12.09.2015	12	Virendra Tiwari	I	Data Representation	Octal number system
14.09.2015	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
15.09.2015	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
16.09.2015	6	Sanjay Kumar Tiwari	III	Memory management	Single Contiguous allocations
18.09.2015				Class Teaching	
19.09.2015	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
21.09.2015	14	Virendra Tiwari	I	Data Representation	BCD number
22.09.2015	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
23.09.2015	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condition
24.09.2015	7	Sanjay Kumar Tiwari	III	Memory management	Partitioned allocation
26.09.2015				Monthly Evaluation	
28.09.2015	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
29.09.2015	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
01.10.2015	15	Virendra Tiwari	II	Combinational circuits	Introduction
03.10.2015	16	Virendra Tiwari	II	Combinational circuits	Half adder
05.10.2015	8	Sanjay Kumar Tiwari	III	Memory management	Relocation partitioned allocation
06.10.2015				Class Teaching	
07.10.2015	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
08.10.2015	18	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
09.10.2015	17	Virendra Tiwari	II	Combinational circuits	Full adder
10.10.2015	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
14.10.2015	9	Sanjay Kumar Tiwari	III	Memory management	Introduction paging
15.10.2015				Class Teaching	
16.10.2015	19	Virendra Tiwari	I	Introduction to programming	Flowchart, dataflow diagram
17.10.2015	20	Virendra Tiwari	I	Programming in C	An overview of C
19.10.2015	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
28.10.2015	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
29.10.2015	10	Sanjay Kumar Tiwari	III	Memory management	Example or problems on paging
30.10.2015				Class Teaching	

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31.10.2015	21	Virendra Tiwari	I	Programming in C	Structure of C program
02.11.2015	22	Virendra Tiwari	I	Programming in C	Storage class specifier
03.11.2015	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
04.11.2015	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
05.11.2015	11	Sanjay Kumar Tiwari	III	Memory management	Segmentation, demand page allocation
06.11.2015				Class Teaching	
07.11.2015	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
14.11.2015	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
16.11.2015	23	Virendra Tiwari	II	Combinational circuit	Edge-triggeredd flip-flop
18.11.2015	24	Virendra Tiwari	II	Combinational circuit	Encoders
19.11.2015	12	Sanjay Kumar Tiwari	III	Processor management	State models, case study of DOS
20.11.2015				Monthly Evaluation	
21.11.2015	25	Virendra Tiwari	I	Control Statement	True & false in C
23.11.2015	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
24.11.2015	25	Virendra Tiwari	II	Cobinational circuits	Decoders
25.11.2015	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
26.11.2015	13	Sanjay Kumar Tiwari	III	Processor management	Job schedulers and its type, Windows
					Operating System
27.11.2015				Class Teaching	
28.11.2015	27	Virendra Tiwari	I	Control statements	For statement, while statement
30.11.2015	28	Virendra Tiwari	I	Control statements	do-while & break
01.12.2015	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
03.12.2015	28	Virendra Tiwari	II	Combinational circuits	Shift Register
07.12.2015	14	Sanjay Kumar Tiwari	III	Processor Management	Process Scheduling
08.12.2015				Class Teaching	
09.12.2015	29	Virendra Tiwari	I	Control Statement	Contimute exit ()& goto
11.12.2015	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
12.12.2015	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
14.12.2015	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
15.12.2015	15	Sanjay Kumar Tiwari	III	Processor management	Job and process synchronisation

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16.12.2015				Class Teaching	
17.12.2015	31	Virendra Tiwari	I	Basic I/O	Concept of function
18.12.2015	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
19.12.2015	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
21.12.2015	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
22.12.2015	16	Sanjay Kumar Tiwari	III	Processor Management	Race conditions, Scheduling algorithm &
					its related problems
28.12.2015				Monthly Evaluation	
29.12.2015	33	Virendra Tiwari	I	Basic I/O	Recursion
30.12.2015	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
31.12.2015	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
01.01.2016	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
02.01.2016	17	Sanjay Kumar Tiwari	III	Processor management	Introduction dead lock, Dead lock detection
					Recovery & Prevention
04.01.2016				Class Teaching	
06.01.2016	35	Virendra Tiwari	I	Array	Introduction
07.01.2016	36	Virendra Tiwari	I	Array	One dimensional
08.01.2016	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
10.01.2016	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
11.01.2016	18	Sanjay Kumar Tiwari	III	Device Management	Introduction & its different technique
12.01.2016				Class Teaching	
13.01.2016	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
16.01.2016	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators,
					Pointer expressions, pointer & array
18.01.2016	37	Virendra Tiwari	II	Memory	Memory hierarchy, RAM, SRAM, DRAM
19.01.2016	38	Virendra Tiwari	II	Memory	Memory write ability & storage performance
20.01.2016	19	Sanjay Kumar Tiwari	III	Device management	I/O Device storage device, logical &
					physical, file system
21.01.2016				Class Teaching	
22.01.2016	39	Virendra Tiwari	I	Pointer	Strings & string library function

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23.01.2016	40	Virendra Tiwari	I	Structure of unions	Concepts, Structure & function array	
					structure, structure pointer, Nested structure	
25.01.2016	39	Virendra Tiwari	II	Memory	Memory units, EPROM, EEPROM,	
					flash memory	
27.01.2016	40	Virendra Tiwari	II	Memory	Memory type-ROM, Masked programmed ROM	
					Cache memory, virtual memory concept	
28.01.2016	20	Sanjay Kumar Tiwari	III	Information management	File system, case study of DOS & window	
					operating system	
29.01.2016				Monthly Evaluation		
30.01.2016	41	Virendra Tiwari	I	File management	Definitions, opening & closing of a file	