

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-तीन			पाठ्यक्रम योजना : सत्र-२०१७-१८		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
17.07.17	1	Virendra Tiwari	I	Principle of comp. design	Basic components of computer system
18.07.17	2	Virendra Tiwari	I	Principle of Comp.design	Overview of Construction of ALU
19.07.17	1	Dr.Sanjay Kumar Tiwari	III	Object Oriented System	Introduction to object oriented System
20.07.17	2	Dr.Sanjay Kumar Tiwari	III	Object Oriented System	Overview of object oriented System
21.07.17	1	Virendra Tiwari	II	DBMS	Concept of Database and its evaluation
21.07.17	1	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Transmission System
22.07.17				CLASS TEACHING	
22.07.17	2	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Purpose, Components Source Transmitter
24.07.17	3	Virendra Tiwari	I	Principle Comp. Design	Overview of Operation of ALU
25.07.17	4	Virendra Tiwari	I	Register Transfer &	Register Transfer Language
				Micro Operator	
26.07.17	3	Dr.Sanjay Kumar Tiwari	III	OOS	Abstract data types
27.07.17	4	Dr.Sanjay Kumar Tiwari	III	OOS	Inheritance
29.07.17	2	Virendra Tiwari	IV	DBMS	Data Abstraction
29.07.17	3	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Data Transmission: Frequency
31.07.17				CLASS TEACHING	
01.08.17	5	Virendra Tiwari	I	Register Transfer &	Bus & Memory Transfer
				micro-Operater	
02.08.17	6	Virendra Tiwari	I	Ragister Transfer &	Anumictic micro-operations
				micro - operator	
03.08.17	5	Dr.Sanjay Kumar Tiwari	III	OOS	Polymorphism
04.08.17	6	Dr.Sanjay Kumar Tiwari	III	OOS	Object Identity
04.08.17	5	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Spectrem & Band Width
05.08.17	4	Virendra Tiwari	II	DBMS	Data integration
05.08.17	5	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Spectrem & Band Width
08.08.17				CLASS TEACHING	
09.08.17	7	Virendra Tiwari	I	Register Transfer &	Logic micro operators
				micro-operator	
10.08.17	8	Virendra Tiwari	I	Register Transfer &	Shift micro operators
				micro-operator	

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11.08.17	7	Dr.Sanjay Kumar Tiwari	III	OOS	Object modling concepts
11.08.17	6	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Anolog Data Transmission
12.08.17	8	Dr.Sanjay Kumar Tiwari	III	OOS	Object oriented programing language
12.08.17	7	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Digital Data Tansmission
14.08.17	4	Virendra Tiwari	II	DBMS	Data Models, Components of DBMS
16.08.17				CLASS TEACHING	
17.08.17	9	Virendra Tiwari	I	Register Transfer	Bus Structure miltiple Bus Hierarchies
18.08.17	10	Virendra Tiwari	I	Register Transfer	Element of Bus Disign
18.08.17	8	Dr.Sanjay Kumar Tiwari	IV	Data Communication System	Data and Signals
19.08.17	9	Dr.Sanjay Kumar Tiwari	III	OOS	Object oriented programing
19.08.17	9	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	Anolog & Digital Signaling of anolog &
					digital data
21.08.17	10	Dr.Sanjay Kumar Tiwari	III	C++ Programing Language	Overview of C++ Programing
22.08.17	5	Virendra Tiwari	II	DBMS	Three level of Architecture of DBMS
23.08.17				CLASS TEACHING	
24.08.17	11	Virendra Tiwari	I	Register Transfer &	PCI Bus Structure
				Micro-Operator	
25.08.17	12	Virendra Tiwari	I	Memory	Linear Structure Memory Organisation
					& Coders
25.08.17	10	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	Modem
26.08.17	11	Dr.Sanjay Kumar Tiwari	III	C++ Programming	Support for data Obstraction
26.08.17	11	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	Modulation Techniques
28.08.17	12	Dr.Sanajay Kumar Tiwar	III	OOS (C++)	Support for object oriented programing
29.08.17	6	Virendra Tiwari	II	DBMS	Data Models Implementation
31.08.17				CLASS TEACHING/TEST	
01.09.17	13	Virendra Tiwari	I	Memory`	Dimension of Memory Access
01.09.17	12	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	CODEC, Digital Transmitter
02.09.17	14	Virendra Tiwari	I	Memory	Connectivity memory chip to a computer
					trees
05.9.17	13	Dr.Sanjay Kumar Tiwari	III	C++ Programming Langauage	Declaration & Constatnts

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06.09.17	14	Dr.Sanjay Kumar Tiwari	III	C++ Programming Language	Declartion & Contants
07.09.17	7	Virendra Tiwari	II	DBMS	Relation Data
08.09.17				CLASS TEACHING	
08.09.17	13	Dr.Sanjay Kumar Tiwari	IV	Data & Signals	Transmission Impairments
09.09.17	15	Virendra Tiwari	I	Memory	Magnetic Disk, Floppy Disk
09.09.17	14	Dr.Sanjay Kumar Tiwari	IV	Data & signal	Attemation & Attemation distrortion
11.09.17	16	Virendra Tiwari	I	Memory	Magnetic Tape
12.09.17	15	Dr.Sanjay Kumar Tiwari	III	C++ Programing Language	Function & File
13.09.17	16	Dr.Sanjay Kumar Tiwari	III	C++ Programing Language	Linkages
14.09.17	8	Virendra Tiwari	II	DBMS	Network Hierarchical
15.09.17				CLASS TEACHING	
15.09.17	15	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	Delay distrortion, noise
16.09.17	17	Virendra Tiwari	I	I/O Device	Cassettes & Cartridges
16.09.17	16	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	Introduction to network
18.09.17	18	Virendra Tiwari	I	I/O Device	Fundamentals of Keyboards, Printers
19.09.17	17	Dr.Sanjay Kumar Tiwari	III	Classes & Objects	Definition of class
21.09.17	18	Dr.Sanjay Kumar Tiwari	III	Classe & Objects	Class Declaration
22.09.17	9	Virendra Tiwari	II	DBMS	Relation & Manipulation
22.09.17	17	Dr.Sanjay Kumar Tiwari	III	Data & Signal	OSI Reference Model
23.09.17				CLASS TEACHING/TEST	
23.09.17	18	Dr.Sanjay Kumar Tiwari	IV	Data & Signal	TCP /IP Reference mModel
25.09.17	19	Virendra Tiwari	II	I/O Device	CRT & LCD Displays Devices
26.09.17	20	Virendra Tiwari	I	CPU	General Register & Organisation
27.09.17	19	Dr.Sannjay Kumar Tiwari	III	Classes & Objects	Class function definitiion member
03.01.17	20	Dr.Sanjay Kumar Tiwari	III	CLasses & Obects	Defination Outside the class, declartion,
					scope resulation oreater
04.09.17	10	Virendra Tiwari	II	DBMS	Relational Algebra
06.10.17				CLASS TEACHING	
06.10.17	19	Dr.Sanjay Kumar Tiwari	IV	Tansmission Media	Meganetic Media
07.10.17		Virendra Tiwari	I	CPU	Memory Stack

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07.10.17	20	Dr.Sanjay Kumar Tiwari	IV	Transmission Media	Twisted Pair cables
09.10.17	22	Virendra Tiwari	I	CPU	One Address & Two Address Instruc
					tions
10.10.17	21	Dr.Sanjay Kumar Tiwari	III	Class & Object	Private & Public member Functions
11.10.17	22	Dr..Sanjay Kumar Tiwari	III	Clas & Object	Nesting member function creating objetcs
12.10.17	11	Virendra Tiwari	II	DBMS	Relational Calculas SQ2
13.10.17				CLASS TEACHING	
13.10.17	21	Dr.Sanjay Kumar Tiwari	IV	Transmission Media	Baseband & Broadband Coaxial Cables
14.10.17	23	Virendra Tiwari	I	CPU	Data Transfer
14.09.17	22	Dr.Sanjay Kumar Tiwari	IV	Transmission Media	Fiber Optics
16.10.17	24	Virendra Tiwari	I	CPU	Arithmetic Logic & Shift
23.10.17	23	Dr.Sanjay Kumar Tiwari	III	Classes & Objects	Accessing class members
24.10.17	24	Dr.Snajay Kumar Tiwari	III	Calsses & Objects	Accesssing Member Functions
25.10.17	12	Dr.Sanjay Kumar Tiwari	IV	DBMS	Funtional Dependencies Ist & 3rd
					Normal form
26.10.17				CLASS TEACHING/TEST	
27.10.17	25	Virendra Tiwari	I	CPU	Software & Hardware interrupt
27.10.17	23	Dr.Sanjay Kumar Tiwari	IV	Wireless Transmission	Radio Transmission
28.10.17	26	Virendra Tiwari	I	CPU	Arithmetic & Instruction Pipr
28.10.17	24	Dr.Sanjay Kumar Tiwari	IV	Wireless Transmission	Microwave Tranmission
30.10.17	25	Dr.Sanjay Kumar Tiwari	III	Classes & Objects	Arrays of Objects
31.10.17	26	Dr.Sanjay Kumar Tiwari	II	Classes & Objects	Objects as function agreement
01.11.17	13	Virendra Tiwari	II	Relation Database Design	BCNF
02.11.17				CLASS TEACHING	
03.10.17	27	Virendra Tiwari	I	Control Unit	Construction of instruction
03.10.17	25	Dr.Sanjay Kumar Tiwari	IV	Wireless Tranmission	ISDN, ATM
06.10.17	28	Virendra Tiwari	I	Control Unit	Intruccion Cycle Organisation
07.10.17	27	Dr.Sanjay Kumar Tiwari	III	Operator Overloading	Opertaor Function,
					User Defined Type conversion
08.11.17	28	Dr.Sanjay Kumar Tiwari	III	Operator Overloading	Literal , Large Object

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10.11.17	14	Virendra Tiwari	II	FoxPro	Introduction of FoxPro
10.11.17	26	Dr.Sanjay Kumar Tiwari	IV	Wireless Transmission	Data Linklayer, Services, Framing
11.11.17				CLASS TEACHING	
11.11.17	27	Dr.Sanjay Kumar Tiwari	IV	Wireless Transmission	Error control, error detecting & correcting codes
13.11.17	29	Virendra Tiwari	I	Control unit	Control Register
14.11.17	30	Virendra Tiwari	I	Control unit	Execution cycle organisation of control registers
15.11.17	29	Dr.Sanjay Kumar Tiwari	III	Operator Overloading	Assignment & Initialisation Sub-scripting
16.11.17	30	Dr.Sanjay Kumar Tiwari	I	Operator Overloading	Function call deferencing increment & decrement
17.11.17	15	Virendra Tiwari	II	FoxPro	Database Construction
17.11.17	31	Dr.Sanjay Kumar Tiwari	III	Operator Overloading	A string class
18.11.17				CLASS TEACHING	
18.11.17	28	Dr.Sanjay Kumar Tiwari	IV	Wireless Transmission	Data links protocols
20.11.17	31	Virenda Tiwari	I	Control unit	Sequence of operation of control register
21.11.17	32	Virendra Tiwari	I	Control unit	Controlling arithmetic operators
22.11.17	32	Dr.Sanjay Kumar Tiwari	III	Operator overloading	Inheritance through extending classes
23.11.17	33	Dr.Sanjay Kumar Tiwari	III	Inheritance through	Derived class, defining derived classes
				extending classes	
24.11.17	16	Virendra Tiwari	II	FoxPro	Searching
24.11.17	29	Dr.Sanjay Kumar Tiwari	III	Wireless Transmission	Stop & wait protocol sliding window protocol
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25.11.17	30	Dr.Sanjay Kumar Tiwari	IV	HDL	Static & dynamic channel allocation in
					Lans
27.11.17	33	Virendr Tiwari	I	Control Unit	Branch
28.11.17	34	Virendra Tiwari	I	Control Unit	Arithmetic operators
29.11.17	34	Dr.Sanjay Kumar Tiwari	III	Inheritance through	

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				extending classes	Visibilty mode, Signal Inheritance
30.11.17	35	Dr.Sanjay Kumar Tiwari	III	Streaming Templates &	I/O Formatting files & streams
				design of libral	
01.12.17	17	Virendra Tiwari	II	Foxpro	Sorting
01.12.17	31	Dr.Sanjay Kumar Tiwari	IV	HDLCE	Static & Dynamaic Channel Allocation in
					LANs
02.12.17				CLASS TEACHING	
02.12.17	32	Dr.Sanjay Kumar Tiwari	IV	Multiple access protocol	ALOHA, Congestion control algos
03.12.17	35	Viendra Tiwari	I	Control Unit	Skip or Jump Instruction
06.12.17	36	Virendra Tiwari	I	Control Unit	Register Language
07.12.17	36	Dr.Sanjay Kumar Tiwari	III	Steam Temples	C-I/O designof libratries
08.12.17	37	Dr.Sanjay Kumar Tiwari	III	OOA & Design	Object oriented analysis system design
08.12.17	33	Dr.Sanjay Kumar Tiwari	IV	Multipe Access Protocol	CSMA/ CD, IEEE Standards 802.3
					subnets.
09.12.17	18	Virendra Tiwari	II	FoxPro	Indexing
09.12.17	34	Dr.Sanjay Kumar Tiwari	IV	Multiple Access Protocol	Ethernet, 802.4, Token Bus
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12.12.17	37	Virendra Tiwari	I	Control Unit	Micro-Programming
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14.12.17	38	Dr.Sanjay Kumar Tiwari	III	OOA & Design	Object Design
15.12.17	39	Dr.Sanjay Kumar Tiwari	III	OOA & Design	Semantic
15.12.17	35	Dr.Sanjay Kumar Tiwari	IV	Multiple Access Protocol	805.5 Token ring, Bridges
				Protocol	
16.12.17	19	Virendra Tiwari	II	Foxpro	Updation
16.12.17	36	Dr.Sanjay Kumar Tiwari	IV	MAP	Routers, Gateways Routing algos
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19.12.17	39	Virendra Tiwari	I	Control Unit	Micro Programming
20.12.17	40	Virendra Tiwari	I	Control Unit	Program
21.12.17	31	Dr.Sanjay Kumar Tiwari	III	OOA & Design	Entity, Relationship
22.12.17	31	Dr.Sanjay Kumar Tiwari	III	OOA & Design	Modeling

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23.12.17	20	Virendra Tiwari	II	Foxpro	Data manage
23.12.17	38	Dr.San jay Kumar Tiwari	IV	MAP	TCP/IP Protocol
26.12.17				CLASS TEACHING/TEST	
27.12.17	41	Virendra Tiwari	I	Control Unit	Micro-Prrogramming
28.12.17	42	Virendra Tiwari	I	Control Unit	Shift Instruction
29.12.17	42	Dr.Sanjay Kumar Tiwari	III	O O A & Design	BOOCH's OOA, OOD approach
29.12.17	42	Dr.Sanjay Kumar Tiwari	IV	MAP	IP Addressing
30.12.17	43	Dr.Sanjay Kumar Tiwari	III	OOA & Design	Semantic Rule
30.12.17	40	Dr.Sanjay Kumar Tiwari	IV	MAP	IP Addressing numericals
01.01.18	21	Virendra Tiwari	II	FoxPro	Report
02.01.18				CLASS TEACHING	
03.01.18	43	Virendra Tiwari	II	Control Unit	Register Language Controlling
					Arithmetic Operation
04.01.18	44	Virendra Tiwari	I	Control Unit	Revision
05.01.18	44	Dr.Sanjay Kumar Tiwari	III	O O A & Design	Overview of Existing Methodlogy
05.01.18	41	Dr.Sanjay Kumar Tiwari	IV	MAP	Revision
06.01.18	45	Dr.Sanjay Kumar Tiwari	III	O O A & Design	Overview of Existing Methodlogy
06.01.18	42	Dr.Sanjay Kumar Tiwari	IV	MAP	Problems
08.01.18	41	Virendra Tiwari	III	Fox Pro	Screen Desigining, Progamming
09.01.18				CLASS TEACHING	
10.01.18	45	Virendra Tiwari	I	Control Unit	Revision
11.01.18	46	Virendra Tiwari	I	Control Unit	Revision
12.01.18	46	Dr.Sanjay Kumar Tiwari	III	O O A & Design	Revision
12.01.18	43	Dr.Sanjay Kumar Tiwari	IV	MAP	Problem
12.01.18	47	Dr.Sanjay Kumar Tiwari	III	O O A & Design	Revision