

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-तीन			पाठ्यक्रम योजना : सत्र-२०१६-१७		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
16.07.16	1	Virendra Tiwari	I	Principle of comp. design	Basic components of computer system
18.07.16	2	Virendra Tiwari	I	Principle of Comp.design	Overview of Construction of ALU
19.07.16	1	Sanjay Kumar Tiwari	III	OOS	Introduction to object oriented System
20.07.16	2	Sanjay Kumar Tiwari	III	OOS	Overview of object oriented System
21.07.16	1	Virendra Tiwari	II	DBMS	Concept of Database and its evaluatio
22.07.16				Class Teaching	
22.07.16	1	Sanjay Kumar Tiwari	IV	Data Communication System	Transmission System
23.07.16	3	Virendra Tiwari	I	Principle Comp. Design	Overview of Operation of ALU
23.07.16	2	Sanjay Kumar Tiwari	IV	Data Communication System	Purpose, Components Source Transmitter
25.07.16	4	Virendra Tiwari	I	Register Transfer &	Register Transfer Language
				Micro Operator	
26.07.16	3	Sanjay Kumar Tiwari	III	OOS	Abstract data types
27.07.16	4	Sanjay Kumar Tiwari	III	OOS	Inheritance
28.07.16	2	Virendra Tiwari	II	DBMS	Data Abstraction
29.07.16				Class Teaching	
29.07.16	3	Sanjay Kumar Tiwari	IV	Data Communication System	Data Transmission : Frequency
30.07.16	5	Virendra Tiwari	I	Register Transfer &	Bus & Memory Transfers
				micro & Operater	
30.07.16	4	sanjay kumar Tiwari	IV	Data Communication System	Receiver & Designation micro & Operater
01.08.16	6	Virendra Tiwari	I	Register Transfer &	Anumictic micro-operations
				micro & Operater	
02.08.16	5	Sanjay Kumar Tiwari	III	OOS	Polymorphism
03.08.16	6	Sanjay Kumar Tiwari	III	OOS	Object Identity
04.08.16	3	Virendra Tiwari	II	DBMS	Data integration
05.08.16				Class Teaching	
05.08.16	5	Sanjay Kumar Tiwari	IV	Data Communication System	Spectrem & Band Width
08.08.16	7	Virendra Tiwari	I	Register Transfer &	Logic micro operators
				micro-operator	
09.08.16	8	Virendra Tiwari	I	Register Transfer Language	Shift micro operators
10.08.16	7	Sanjay Kumar Tiwari	III	OOS	Object modling concepts

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11.08.16	8	Sanjay Kumar Tiwari	III	OOS	Object oriented Programming Language
12.08.16	4	Virendra Tiwari	II	DBMS	Data Models, Components of DBMS
12.08.16	6	Sanjay kumar Tiwari	IV	Data Communication System	Anolog Data Transmission
13.08.16				Class Teaching	
13.08.16	7	Sanjay Kumar Tiwari	IV	Data Communication System	Digital Data Transmission
15.08.16	9	Virendra Tiwari	I	Register Transfer	Bus Structure multiple Bus Trierarchies
16.08.16	10	Virendra Tiwari	I	Register Transfer	Element of Bus Disign
17.08.16	9	Sanjay Kumar Tiwari	III	OOS	Object oriented Programming Language
19.08.16	10	Sanjay Kumar Tiwari	III	C++ Programming Language	Overview of C++ Programming Paradigin
19.08.16	8	Sanjay Kumar Tiwari	IV	Data Communication System	Data & Signal
20.08.16	5	Virendra Tiwari	II	DBMS	The Three level architecture of DBMS
20.08.16	9	Sanjay Kumar Tiwari	IV	Data & Signal	Anolog & Digital Siguating of anolog & digital data
22.08.16				Class teaching	
23.08.16	11	Virendra Tiwari	I	Register Transfer &	PCI bus structure
				micro-operator	
24.08.16	12	Virendra Tiwari	I	Memory	Linear selected Memory organisation &
					Coders
26.08.16	11	Sanjay Kumar Tiwari	III	C++ Programming Language	Support for data obstraction
26.08.16	10	Sanjay Kumar Tiwari	IV	Data & Signal	Modem
27.08.16	12	Sanjay Kumar Tiwari	III	OOS (C+1)	Support for object oriented Programming
27.08.16	11	Sanjay Kumar Tiwari	IV	Data & Signal	Modulation Techniques
29.08.16	6	Virendra Tiwari	II	DBMS	Data Models implementtation
30.08.16				Test	
31.08.16	13	Virendra Tiwari	I	Memory	Dimension of memory access
01.09.16	14	Virendra Tiwari	I	Memory	Connectivity memory Chips to a
					computer trees
02.09.16	13	Sanjay Kumar Tiwari	III	C++ Programming Language	Declaration & Constants

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02.09.16	12	Sanjay Kumar Tiwari	IV	Data & Signal	CODEC, Digital transmitter
03.09.16	14	Sanjay Kumar Tiwari	III	C++ Programming Language	Expression & Statement
03.09.16	13	Sanjay Kumar Tiwari	IV	Data & Signal	Transmission impairments
05.09.16	7	Virendra Tiwari	II	DBMS	Relational Data
06.09.16				class Teaching	
07.09.16	15	Virendra Tiwari	I	Memory	Magnetic disk memories floppy disk
08.09.16	16	Virendra Tiwari	I	Memory	Magnetic tape
09.09.16	15	Sanjay Kumar Tiwari	III	C++ Programming Language	Functions & Files
09.09.16	14	Sanjay Kumar Tiwari	IV	Data & Signal	Attenuation & attenuation distortion
10.09.16	16	Sanjay Kumar Tiwari	III	C++ Programming Language	Linkages
10.09.16	15	Sanjay Kumar Tiwari	IV	Data & Signal	Delay Distortion, noise
14.09.16	8	Virendra Tiwari	II	DBMS	Network Hierarchical
15.09.16				CLASS TEACHING	
16.09.16	17	Virendra Tiwari	I	I/O Devices	Cassettes & Cartridges
16.09.16	16	Sanjay Kumar Tiwari	IV	Data & Signal	Introduction to network
20.09.16	18	Virendra Tiwari	I	I/O Devices	Fundamentals of Keyboards, Printers
21.09.16	17	Sanjay Kumar Tiwari	III	Classes & Objects	Class Declaration
22.09.16	18	Sanjay Kumar Tiwari	III	Classes & objects	Class function definition member
23.09.16	9	Virendra Tiwari	II	DBMS	Relational & Manipulation
23.09.16	17	Sanjay Kumar Tiwari	IV	Data & Signal	OSI Reference Model
24.09.16				Test	
24.09.16	18	Sanjay Kumar Tiwari	IV	Data & Signal	TCP/IP Reference Model
26.09.16	19	Virendra Tiwari	I	I/O Devices	CRT & LCD Displays Devices
27.09.16	20	Virendra Tiwari	I	CPU	General Register & organisation
28.09.16	19	Sanjay Kumar Tiwari	III	Classes & Objects	Definition of class
29.09.16	20	Sanjay Kumar Tiwari	III	Classes & objects	Definition outside the class declaration,
					scope regulation or eater
03.10.16	10	Virendra Tiwari	II	DBMS	Relational Algebra
04.10.16				Class Teaching	
05.10.16	21	Virendra Tiwari	I	CPU	Memory stack

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06.10.16	22	Virendra Tiwari	I	CPU	One address & Two address Instructions
07.10.16	21	Sanjay Kumar Tiwari	III	Classes object	Private & Public member function
07.10.16	19	Sanjay Kumar Tiwari	IV	Transmissions media	Magnetic media
08.10.16	22	Sanjay Kumar Tiwari	III	Classes object	Nesting member function creating objects
08.10.16	20	Sanjay Kumar Tiwari	IV	Transmission media	Twisted Pair Cables
14.10.16	11	Virendra Tiwari	II	DBMS	Relational Calculus SQL
14.10.16	21	Sanjay Kumar Tiwari	IV	Transmission media	Baseband & Broadband coaxial cables
15.10.16				CLASS TEACHING/	
15.10.16	22	Sanjay Kumar Tiwari	IV	Transmission media	Fiber optics
17.10.16	23	Virendra Tiwari	I	CPU	Data Transfer
18.10.16	24	Virendra Tiwari	I	CPU	Arithmetic Logic & shift instructions
19.10.16	23	Sanjay Kumar Tiwari	III	Classes & Objects	Accessing class members
20.10.16	24	Sanjay Kumar Tiwari	III	Classes & Object	Accessing members function
21.10.16	12	Virendra Tiwari	II	Relation Database design	Functional Dependencieo Ist & 3rd
					Normal form
21.10.16	23	Sanjay Kumar Tiwari	IV	Wireless Transmission	Radio Transmission
22.10.16				TEST	
22.10.16	24	Sanjay Kumar Tiwari	IV	Wireless Transmission	Microwave Transmission
24.10.16	25	Virendra Tiwari	I	CPU	Software & Hardware interrupt
25.10.16	26	Virendra Tiwari	I	CPU	Arithmetic & Instruction pipe line
26.10.16	25	Sanjay Kumar Tiwari	III	Classes & Objects	Arrays of objects
02.11.16	26	Sanjay Kumar Tiwari	III	Classes & Object	Objects as function agreement
03.11.16	13	Virendra Tiwari	II	Relational Database Design	BCNF
04.11.16				CLASS TEACHING	
04.11.16	25	Sanjay Kumar Tiwari	IV	Wireless Transmission	ISDN, ATM
05.11.16	27	Virendra Tiwari	I	Control Unit	Coustruction of instraction word
05.11.16	26	Sanjay Kumar Tiwari	IV	Wireless Transmission	Data link layer, services, Framing
08.11.16	28	Virendra Tiwari	I	Control Unit	Instruction Cycle Organisation
09.11.16	27	Sanjay Kumar Tiwari	III	Operator overloading	Operator function user defined type
					conversion

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10.11.16	28	Sanjay Kumar Tiwari	III	Operator Overloading	Literal, Large object
11.11.16	14	Viendra Tiwari	II	Fox Pro	Introduction of Fox Pro
11.11.16	27	Sanjay Kumar Tiwari	IV	Wireless Transmission	Error control error-detecting & correcting codes
12.11.16				CLASS TEACHING/ TEST	
12.11.16	28	Sanjay Kumar Tiwari	IV	Wireless Transmission	Data Link Protocols
15.11.16	29	Viendra Tiwari	I	Control Unit	Control register
17.11.16	30	Virendra Tiwari	I	Control Unit	Execution cycle organisation of control Registers
18.11.16	29	Sanjay Kumar Tiwari	III	Operator Overloading	Assignment & Initialisation Sub scripiting
18.11.16	29	Sanjay Kumar Tiwari	IV	Wireless Transmission	Stop-and-wait Protocol, Sliding Window Protocol
19.11.16	30	Sanjay Kumar Tiwari	III	Operator Overloading	Function call Deferencing Increment & Decrement
19.11.16	30	Sanjay Kumar Tiwari	IV	HDLC	Static & Dynamic channel allocation in LANS
21.11.16	15	Virendra Tiwari	II	Foxpro	Database construction
22.11.16				CLASS TEACHING	
23.11.16	31	Virendra Tiwari	I	Control Unit	Sequence of operating of control registers
25.11.16	32	Virendra Tiwari	I	Control Unit	Controlling Arithmetic operators
25.11.16	31	Sanjay Kumar Tiwari	IV	Multipal Access Protocol	ALOHA, congestion control algos
26.11.16	31	Sanjay Kumar Tiwari	III	Operator Overloading	A String Class
26.11.16	32	Sanjay Kumar Tiwari	IV	HDLC	Static & Dynamic Channel allocation in MANS
28.11.16	32	Sanjay Kumar Tiwari	III	Operator Overloading	Inheritance Through extending classes
29.11.16	16	Virendra Tiwari	II	Foxpro	Searching
30.11.16				CLASS TEACHING	
01.12.16	33	Virendra Tiwari	I	Control Unit	Skip or Jump instruction

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02.12.16	34	Virendra Tiwari	I	Control Unit	Register Language
02.12.16	33	Sanjay Kumar Tiwari	IV	Multiple access protocols	Aloha
06.12.16	33	Sanjay Kumar Tiwari	III	Inheritance through	Drived class, Defining Derived classes
				extendin classes	
07.12.16	34	Sanjay Kumar Tiwari	III	Inheritance through	Visibility Modes, Signal Inheritance
				extendin classes	
08.12.16	17	Virendra Tiwari	II	Foxpro	Sorting
09.12.16				CLASS TEACHING	
09.12.16	34	Sanjay Kumar Tiwari	IV	Multipal Access Protocol	CSMA/CD,IEEE Standards 802.3 subnets
12.12.16	35	Virendra Tiwari	I	Control Unit	Branch
14.12.16	36	Virendra Tiwari	I	Control Unit	Arithmetic Operators
15.12.16	35	Sanjay Kumar Tiwari	III	Stream templates & Design	O/I formating files & streams
				of Librales	
16.12.16	36	Sanjay Kumar Tiwari	III	Steam Temples	C-I/O Design of Libraties
16.12.16	35	Sanjay Kumar Tiwari	IV	Multipal Access Protocal	Eathernet, 802.4 Token Bus
17.12.16	18	Virendra Tiwari	II	Foxpro	indexing
17.12.16	36	Sanjay Kumar Tiwari	IV	MAP	Routers, Gateways Routing
19.12.16				CLASS TEACHING	
20.12.16	37	Virendra Tiwari	I	Control Unit	Micro- Programming
21.12.16	38	Virendra Tiwari	I	Control Unit	Micro- Programming
22.12.16	37	Sanjay Kumar Tiwari	III	OOA & Design	Object oriented analysis system design
24.12.16	38	Sanjay Kumar Tiwari	III	OOA & Design	Object design
24.12.16	37	Sanjay Kumar Tiwari	IV	Multiples access protocols	802-5 Taken Ring, Bridges
26.12.16	19	Virendra Tiwari	II	Foxpro	Updation
27.12.16				CLASS TEACHING/Test	
28.12.16	39	Virendra Tiwari	I	Control Unit	Micro- Programming
29.12.16	40	Virendra Tiwari	I	Control Unit	Program
30.12.16	39	Sanjay Kumar Tiwari	III	OOA & Design	Semantic
30.12.16	38	Sanjay Kumar Tiwari	IV	MAP	Internet working
31.12.16	40	Sanjay Kumar Tiwari	III	OOA & Design	Entity, Relationship

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