

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-तीन			पाठ्यक्रम योजना : सत्र-२०१४-१५		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
16.07.14	1	Virendra Tiwari	I	Principle of comp. design	Basic components of computer system
17.07.14	2	Virendra Tiwari	I	Principle of Comp.design	Overview of Construction of ALU
18.07.14	1	Sanjay Kumar Tiwari	III	OOS	Introduction to object oriented System
19.07.14	2	Sanjay Kumar Tiwari	III	OOS	Overview of object oriented System
21.07.14	1	Virendra Tiwari	II	DBMS	Concept of Database and its evaluation
21.07.14	1	Sanjay Kumar Tiwari	IV	Data Communication System	Purpose, Components Source Transmitter
22.07.14				CLASS TEACHING	
22.07.14	2	Sanjay Kumar Tiwari	IV	Data Communication System	Transmission System
23.07.14	3	Virendra Tiwari	I	Principle Comp. Design	Overview of Operation of ALU
24.07.14	4	Virendra Tiwari	I	Register Transfer &	Register Transfer Language
				Micro Operator	
26.07.14	3	Sanjay Kumar Tiwari	III	OOS	Abstract data types
28.07.14	4	Sanjay Kumar Tiwari	III	OOS	Inheritance
28.07.14	3	Sanjay Kumar Tiwari	IV	Data Communication System	Receiver & Designation
30.07.14	2	Virendra Tiwari	II	DBMS	Data Abstraction
31.07.14				CLASS TEACHING	
02.08.14	5	Virendra Tiwari	I	Register Transfer &	Bus & Memory Transfers
				micro & Operater	
04.08.14	6	Virendra Tiwari	I	Register Transfer &	Anumictic micro-operations
				micro & Operater	
04.08.14	4	Sanjay Kumar Tiwari	IV	Data Communication System	Data Transmission : Frequency
05.08.14	5	Sanjay Kumar Tiwari	III	OOS	Polymorphism
05.08.14	5	Sanjay Kumar Tiwari	IV	Data Communication System	Spectrem & Band Width
06.08.14	6	Sanjay Kumar Tiwari	III	OOS	Object Identity
07.08.14	3	Virendra Tiwari	II	DBMS	Data integration
08.08.14				CLASS TEACHING	
09.08.14	7	Virendra Tiwari	I	Register Transfer &	Logic micro operators
				micro-operator	
11.08.14	8	Virendra Tiwari	I	Register Transfer Language	Shift micro operators
11.08.14	6	Sanjay Kumar Tiwari	IV	Data Communication System	Anolog Data Transmission

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12.08.14	7	Sanjay Kumar Tiwari	III	OOS	Object modling concepts
12.08.14	7	Sanjay Kumar Tiwari	IV	Data Communication System	Digital Data Transmission
13..08.14	8	Sanjay Kumar Tiwari	III	DBMS	Component of DBMS The Three level
					architecture of DBMS
16.08.14				CLASS TEACHING	
19.08.14	9	Virendra Tiwari	I	Register Transfer	Bus Structure multiple Bus Trierarchies
19.08.14	8	Sanjay Kumar Tiwari	IV	Data Communication System	Data & Signal
20.08.14	10	Virendra Tiwari	I	Register Transfer	Element of Bus Disign
21.08.14	9	Sanjay Kumar Tiwari	III	OOS	Object oriented Programming Language
22.08.14	10	Sanjay Kumar Tiwari	III	OOS	Object oriented data base
23.08.14	5	Virendra Tiwari	II	DBMS	Data Models & Their Implementation
25.08.14				TEST	
25.08.14	9	Sanjay Kumar Tiwari	IV	Data & Signal	Anolog & Digital Siguating of anolog &
					digital data
26.08.14	11	Virendra Tiwari	I	Register Transfer &	PCI bus structure
				micro-operator	
26.08.14	10	Sanjay Kumar Tiwari	IV	Data & Signal	Modem
27.08.14	11	Sanjay Kumar Tiwari	III	C++ Programming Language	Overview of C++ Programming Paradigin
28.08.14	12	Sanjay Kumar Tiwari	III	C++ Programming Language	Support for data obstraction
29.08.14	6	Virendra Tiwari	II	DBMS	Relational
30.08.14	7	Virendra Tiwari	II	DBMS	Network Hierarchical
01.09.14				CLASS TEACHING	
01.09.14	11	Virendra Tiwari	IV	Data & Signal	Modulation Techniques
02.09.14	12	Virendra Tiwari	I	Memory	Linear selected memory organisation&
					coders
02.09.14	12	Sanjay Kumar Tiwari	IV	Data & Signal	DODFC, Digital Transmitter\
03.09.14	13	Virendra Tiwari	I	Memory	Dimension of memory access
04.09.14	13	Sanjay Kumar Tiwari	III	OOS (C+1)	Support for object oriented Programming
05.09.14	14	Sanjay Kumar Tiwari	III	C++ Programming Language	Declaration & Constants
06.09.14	8	Virendra Tiwari	II	DBMS	Relational & Manipulation

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10.09.14				CLASS TEACHING	
12.09.14	14	Virendra Tiwari	I	Memory	Connectivity memory Chips to a
					computer trees
13.09.14	15	Virendra Tiwari	I	Memory	Magnetic disk memories floppy disk
16.09.14	15	Sanjay Kumar Tiwari	III	C++ Programming Language	Declaration & Constants
16.09.14	13	Sanjay Kumar Tiwari	IV	Data & Signal	Transmission impairments
18.09.14	16	Sanjay Kumar Tiwari	III	C++ Programming Language	Expressiona & Statement
19.09.14	9	Virendra Tiwari	II	DBMS	Relational algebra
20.09.14				CLASS TEACHING	
22.09.14	16	Virendra Tiwari	I	Memory	Magnetic tape
22.09.14	14	Sanjay Kumar Tiwari	IV	Data & Signal	Attenuation & attenuation distortion
24.09.14	17	Virendra Tiwari	I	Memory	Cassettes & Cartridges
26.09.14	17	Sanjay Kumar Tiwari	III	C++ Programming Language	Functions & Files
27.09.14	18	Sanjay Kumar Tiwari	III	C++ Programming Language	Linkages
29.09.14	10	Virendra Tiwari	II	DBMS	Relational Calculus SQL
29.09.14	15	Sanjay Kumar Tiwari	IV	Data & Signal	Delay Distortion, noise
30.09.14				TEST	
30.09.14	16	Sanjay Kumar Tiwari	IV	Data & Signal	Introduction to network
01.10.14	18	Virendra Tiwari	I	I/O Devices	Fundamentals of Keyboards, Printers
07.10.14	19	Virendra Tiwari	I	I/O Devices	CRT & LCD Displays Devies
07.10.14	17	Sanjay Kumar Tiwari	IV	Data & Signal	OSI Reference Model
09.10.14	19	Sanjay Kumar Tiwari	III	Classes & Objects	Definition of Class
10.10.14	20	Sanjay Kumar Tiwari	III	Classes & Objects	Class Declaration
11.10.14	11	Virendra Tiwari	II	Relation Database Disign	Functional Dependencies Ist & 3rd
					Normal form
13.10.14				CLASS TEACHING	
13.10.14	18	Sanjay Kumar Tiwari	IV	Data & Signal	TCP/IP Reference Model
14.10.14	20	Virendra Tiwari	I	CPU	General Register & organisation
14.10.14	19	Sanjay Kumar Tiwari	IV	Transmissions media	Magnetic media
15.10.14	21	Virendra Tiwari	I	CPU	Memory Stack

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16.10.14	21	Sanjay Kumar Tiwari	III	Classes & Objects	Class function definition member
					function, definition inside
17.10.14	22	Sanjay Kumar Tiwari	III	Classes & Objects	Definition outside the Class dedaration
					Scope resulation, oreater (::)
18.10.14	12	Virendra Tiwari	II	Relational Database Disign	BCNF
27.10.14				TEST	
27.10.14	20	Sanjay Kumar Tiwari	IV	Data & Signal	Thristed Pair Cables
28.10.14	22	Virendra Tiwari	I	CPU	One address & Two address instructions
28.10.14	21	Sanjay Kumar Tiwari	IV	Data & Signal	Base Bond & Broad Band Coxial Cables
30.10.14	23	Virendra Tiwari	I	CPU	Data Transfer
31.10.14	23	Sanjay Kumar Tiwari	III	Classes Objects	Private & Public member function
01.11.14	24	Sanjay Kumar Tiwari	III	Classes Objects	Nesting member function creating objects
07.11.14	13	Virendra Tiwari	II	Foxpro	Introduction of Foxpro
08.11.14				CLASS TEACHING	
10.11.14	24	Virendra Tiwari	I	CPU	Arithmetic logic & Shift instructions
10.11.14	22	Sanjay Kumar Tiwari	IV	Data & Signal	Fiber optics
11.11.14	25	Virendra Tiwari	I	CPU	Software & Hardware interrupt
					(Only briebe inroduction)
11.11.14	23	Sanjay Kumar Tiwari	IV	Wiveless Transmission	Radiator Transmission
12.11.14	25	Sanjay Kumar Tiwari	III	Classes & Objcets	Accessing Class members
13.11.14	26	Sanjay Kumar Tiwari	III	Classes & Objcets	Accessing member function
14.11.14	14	Virendra Tiwari	II	Foxpro	Database Coustruction
15.11.14				CLASS TEACHING	
17.11.14	26	Virendra Tiwari	I	CPU	Arithmetic & Instruction pipe lines
17.11.14	24	Sanjay Kumar Tiwari	IV	Wireless Transmission	Microwave Transmission
18.11.14	27	Virendra Tiwari	I	Control Unit	Construction of instraction word
18.11.14	25	Sanjay Kumar Tiwari	IV	Wireless Transmission	ISDN, ATM
19.11.14	27	Sanjay Kumar Tiwari	III	Classes & Object	Arrays of Objects
20.11.14	28	Sanjay Kumar Tiwari	III	Classes & Object	Objects as function argerments
21.11.14	15	Virendra Tiwari	II	Foxpro	Searching

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22.11.14				TEST	
25.11.14	28	Virendra Tiwari	I	Control Unit	Instruction Cycle organisation & Control
					registered
25.11.14	26	Sanjay Kumar Tiwari	IV	Wireless Transmission	Data link layer : Series, framing
26.11.14	29	Virendra Tiwari	I	Control Unit	Execution Cycle Organisation of Control
					Registers
27.11.14	29	Sanjay Kumar Tiwari	III	Operator Overloading	Operator function, user defined type
					conversion
28.11.14	30	Sanjay Kumar Tiwari	III	Operator Overloading	Literal, large Object
29.11.14	16	Virendra Tiwari	II	Foxpro	Sorting
01.12.14				CLASS TEACHING	
01.12.14	27	Sanjay Kumar Tiwari	IV	Wireless Transmission	Error Control
02.12.14	30	Virendra Tiwari	I	Control Unit	Sequence of operation of Control registers
02.12.14	28	Sanjay Kumar Tiwari	IV	Wireless Transmission	Error Detecting & Correcting Codes
03.12.14	31	Virendra Tiwari	I	Control Unit	Controlling Arithmetic Operators
06.12.14	31	Sanjay Kumar Tiwari	III	Operator Overloading	Assignments & Initialisation, Sub Scripting
08.12.14	32	Sanjay Kumar Tiwari	III	Operator Overloading	Function Call Deferencing Increment &
					Decrement
08.12.14	29	Sanjay Kumar Tiwari	IV	Wireless Transmission	Data Link Protocols
09.12.14	17	Virendra Tiwari	II	Foxpro	Indexing
09.12.14	30	Sanjay Kumar Tiwari	IV	Wireless Transmission	Stop & Wait Protocol
11.12.14				CLASS TEACHING	
12.12.14	32	Virendra Tiwari	I	Control Unit	Branch
15.12.14	33	Virendra Tiwari	I	control Unit	Skip or Jump instruction
15.12.14	31	Sanjay Kumar Tiwari	IV	Wireless Transmission	Sliding Window Protocol
16.12.14	33	Sanjay Kumar Tiwari	III	Operator Overloading	A String Class
16.12.14	32	Sanjay Kumar Tiwari	IV	UDLC	Static Channel allocation in LANS
17.12.14	34	Sanjay Kumar Tiwari	III	Inheritance Through	
				Extending Classes	
18.12.14	18	Virendra Tiwari	II	Foxpro	Usodation

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20.12.14				CLASS TEACHING	
22.12.14	35	Virendra Tiwari	I	Control Unit	Register Language
22.12.14	33	Sanjay Kumar Tiwari	IV	UD2S	Static Channel Allocation in MANS
23.12.14	35	Sanjay Kumar Tiwari	III	Inheritance Through	Derived Class, Defining Derived Classes
				Extending, Classes	
23.12.14	34	Sanjay Kumar Tiwari	IV	HDL C	Dynamic Channel allocation in LANS
24.12.14	36	Sanjay Kumar Tiwari	III	Inheritance Through	Visibility Modes, Signal inheritance
				Extending Classes	
26.12.14	19	Virendra Tiwari	II	Foxpro	Report
27.12.14				TEST	
29.12.14	36	Virendra Tiwari	I	Control Unit	Microprogramming
29.12.14	35	Sanjay Kumar Tiwari	IV	HDL C	Dynamic Channel allocation in MANS
30.12.14	37	Virendra Tiwari	I	Control Unit	Micro Programming
30.12.14	36	Sanjay Kumar Tiwari	IV	Multiple access protocols	Aloha
31.12.14	37	Sanjay Kumar Tiwari	III	Stream templates &	O/I formatting files & Streams
				Design of libraries	
01.01.15	38	Sanjay Kumar Tiwari	III	Stream Templates	C-I/O Design of libraries
02.01.15	20	Virendra Tiwari	II	Foxpro	Screen Designing
03.01.15				CLASS TEACHING	
06.01.15	38	Virendra Tiwari	I	Control Unit	Microprogramming
06.01.15	37	Sanjay Kumar Tiwari	IV	Multiple access protocols	CSMA/CD
07.01.15	39	Virendra Tiwari	I	Control Unit	Shift instructions
08.01.15	39	Sanjay Kumar Tiwari	III	OOA & Design	Object oriented analysis system design
09.01.15	40	Sanjay Kumar Tiwari	III	OOA & Design	Object Design
10.01.15	21	Virendra Tiwari	II	Foxpro	Programming Concept
12.01.15				CLASS TEACHING	
12.01.15	38	Sanjay Kumar Tiwari	IV	Multiple access protocols	IEEE-Standards 802-3
13.01.15	40	Virendra Tiwari	I	Control Unit	Register Language
13.01.15	39	Sanjay Kumar Tiwari	IV	Multiple access protocols	Ethernet, 802-4, Token Bus
17.01.15	41	Virendra Tiwari	I	Control Unit	Controlling arithmetic operator

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19.01.15	41	Sanjay Kumar Tiwari	III	OOA & Design	Semantic
19.01.15	40	Sanjay Kumar Tiwari	IV	Multiples access protocols	802-5 Taken Ring
20.01.15	42	Sanjay Kumar Tiwari	III	OOA & Design	Entity
20.01.15	41	Sanjay Kumar Tiwari	IV	Multiple access protocols	Bridges
21.01.15	22	Virendra Tiwari	II	Foxpro	Managing number & Data
22.01.15				CLASS TEACHING	
23.01.15	42	Virendra Tiwari	I	Control Unit	Revision
27.01.15	43	Virendra Tiwari	I	Control Unit	Revision
27.01.15	42	Sanjay Kumar Tiwari	IV	Multiple access Protocols	Routers
28.01.15	43	Sanjay Kumar Tiwari	III	OOA & Design	Relationship
27.01.15	44	Sanjay Kumar Tiwari	III	OOA & Design	Modeling
30.01.15	23	Virendra Tiwari	II	Foxpro	Data Problems
31.01.15				TEST	
02.02.15	44	Virendra Tiwari	I	Control Unit	Revision
02.02.15	43	Sanjay Kumar Tiwari	IV	Multiples access protocols	Gateways
03.02.15	45	Virendra Tiwari	I	Control Unit	Revision
03.02.15	44	Sanjay Kumar Tiwari	IV	Multiple access protocols	Rounting alogs
04.02.15	45	Sanjay Kumar Tiwari	III	OOA & Design	Overview of existing meths dology
05.02.15	46	Sanjay Kumar Tiwari	III	OOA & Design	Booch's OOA
06.02.15	24	Virendra Tiwari	II	Foxpro	Record, Data, File
07.02.15				CLASS TEACHING	
09.02.15	47	Sanjay Kumar Tiwari	III	OOA & Design	OOD approach
09.02.15	45	Sanjay Kumar Tiwari	IV	Multiple access protocols	Congestion Control alogs
10.02.15	48	Sanjay Kumar Tiwari	III	OOA & Design	Semantic Rule
10.02.15	46	Sanjay Kumar Tiwari	IV	Multiples access protocols	Internet Working IP Addressing
11.02.15	47	Sanjay Kumar Tiwari	IV	Multiples access protocols	TCP/IP Protocol
12.02.15	47	Sanjay Kumar Tiwari	IV	Multiples access protocols	TCP/IP Protocol
13.02.15	25	Virendra Tiwari	II	Foxpro	Data Refinement