

कक्षा : बी.एस-सी. भाग-एक					विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
01-08-2012	1	Virendra Tiwari	I	Introduction	Definition, characteristic, evaluation, generation classification of computer
03-08-2012	2	Virendra Tiwari	I	Introduction	Types of computer, overview of digital computer
04-08-2012	1	Virendra Tiwari	II	Digital circuits	Logics gates, logic circuits, logic, symbol, logic combinations
06-08-2012	2	Virendra Tiwari	II	Digital circuits	Positive logic, negative & mixed logic
07-08-2012	1	Virendra Tiwari	III	Operative system overview	Introduction
08-08-2012				Class Teaching	
09-08-2012	3	Virendra Tiwari	I	Introduction	Major component of digital computer
11-08-2012	4	Virendra Tiwari	I	Introduction	Hardware
13-08-2012	3	Virendra Tiwari	III	Operating system overview	Computer system structure
14-08-2012	3	Virendra Tiwari	II	Digital circuits	Inverter, Buffer
18-08-2012	4	Virendra Tiwari	II	Digital circuits	AND Gate, NAND Gate, NOR Gate
20-08-2012				Class Teaching	
21-08-2012	5	Virendra Tiwari	I	Introduction	Software, Firmware

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22-08-2012	6	Virendra Tiwari	I	Introduction	Software, Firmware, Science & Management
23-08-2012	3	Virendra Tiwari	III	Operating system overview	Operating system
24-08-2012	7	Virendra Tiwari	I	Data representation	Decimal & Binary no. system
25-08-2012	5	Virendra Tiwari	II	Digital circuits	OR Gate, Exclusive OR & NOR Gate
27-08-2012				Class Teaching	
28-08-2012	6	Virendra Tiwari	II	Digital circuits	Boolean algebra introduction
29-08-2012	8	Virendra Tiwari	I	Date representation	Decimal conversion
30-08-2012	4	Virendra Tiwari	III	Operating system overview	OS functions
01-09-2012	9	Virendra Tiwari	I	Data Representation	Octal number system
03-09-2012	10	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
04-09-2012				Class Teaching	
05-09-2012	7	Virendra Tiwari	II	Digital circuits	Boolean law of intersection, union
06-09-2012	8	Virendra Tiwari	II	Digital circuits	Boolean law of tautology, complement
					double aregation
07-09-2012	5	Virendra Tiwari	III	Operating system overview	Facilities

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08-09-2012	11	Virendra Tiwari	I	Data Representation	BCD number, Gray code
10-09-2012	12	Virendra Tiwari	I	Data Representation	Binary arithmetic
11-09-2012	6	Virendra Tiwari	III	Memory Management	Introduction
12-09-2012	9	Virendra Tiwari	II	Digital circuits	Boolean law of commutation, Association, Distribution
13-09-2012	10	Virendra Tiwari	II	Digital circuits	Boolean law of absorption
14-09-2012	13	Virendra Tiwari	I	Data Representation	Complements, r & (r-1)'s complements
15-09-2012	14	Virendra Tiwari	I	Data Representation	Fixed & floating point representation
18-09-2012				Class Teaching	
19-09-2012	13	Virendra Tiwari	III	Memory Management	Single Contiguous allocation
20-09-2012	11	Virendra Tiwari	II	Digital circuits	De Morgan's theorem
21-09-2012	12	Virendra Tiwari	II	Digital circuits	Simplification of Boolean Algebraic expression
22-09-2012	14	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
24-09-2012	15	Virendra Tiwari	I	Introduction to programming	Flowcharts
25-09-2012				Class Teaching	
26-09-2012	8	Virendra Tiwari	III	Memory management	Partitioned allocation

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27-09-2012	16	Virendra Tiwari	I	Introduction to programming	Flowcharts Rules, Advantage & Limitation of flowcharts
01-10-2012	13	Virendra Tiwari	II	Digital circuits	Translating algebra to logic
04-10-2012	14	Virendra Tiwari	II	Digital circuits	Universal logic Gate
05-10-2012	17	Virendra Tiwari	I	Programming in C	An overview of C, structure of C program
06-10-2012				Class Teaching	
08-10-2012	9	Virendra Tiwari	III	Memory Management	Relocatable partitioned allocation
11-10-2012	18	Virendra Tiwari	I	Programming in C	Storage class specific & data types
12-10-2012	19	Virendra Tiwari	I	Programming in C	Variable declaration, Operator & Expressing
13-10-2012	15	Virendra Tiwari	II	Digital circuits	Truth table from circuit, logic circuit from truth table
17-10-2012	16	Virendra Tiwari	II	Digital circuits	Karnaugh map introduction
18-10-2012				Class Teaching	
19-10-2012	10	Virendra Tiwari	III	Memory Management	Paging
20-10-2012	20	Virendra Tiwari	I	Control statements	True & False statements in C
29-10-2012	21	Virendra Tiwari	I	Control statements	If switch statements in C

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30-10-2012	11	Virendra Tiwari	III	Memory Management	Segmentation
31-10-2012	17	Virendra Tiwari	II	Digital circuits	Minterms and Maxterms
01-11-2012				Class Teaching	
02-11-2012	18	Virendra Tiwari	II	Digital circuits	Two variable karnaugh map
03-11-2012	22	Virendra Tiwari	I	Control statements	For, while, do-while statement in C
05-11-2012	23	Virendra Tiwari	I	Control statements	Break, exit C, continue & got statement in C
06-11-2012	12	Virendra Tiwari	III	Memory management	Demand paged allocation
07-11-2012	24	Virendra Tiwari	I	Basic I/O	Formatted of informatted I/O
08-11-2012				Class Teaching	
09-11-2012	19	Virendra Tiwari	II	Digital circuits	Three variable karnaugh map
16-11-2012	20	Virendra Tiwari	II	Digital circuits	Four variable karnaugh map
17-11-2012	25	Virendra Tiwari	I	Basic I/O	Concepts of programming & functions with
					example
19-11-2012	13	Virendra Tiwari	III	Processor Management	Introduction
20-11-2012	26	Virendra Tiwari	I	Basic I/O	Parameter Passing Technique

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21-11-2012				Class Teaching	
22-11-2012	27	Virendra Tiwari	I	Basic I/O	Recursion
23-11-2012	21	Virendra Tiwari	II	Digital circuits	Don't care states
26-11-2012	22	Virendra Tiwari	II	Combinational circuit	Half adder
27-11-2012	14	Virendra Tiwari	III	Processor management	State models
29-11-2012	28	Virendra Tiwari	I	Arrays	One Dimensional Arrays
30-11-2012				Class Teaching	
01-12-2012	29	Virendra Tiwari	I	Arrays	Two Dimensional Arrays
03-12-2012	15	Virendra Tiwari	III	Processor Management	Job schedules
07-12-2012	23	Virendra Tiwari	II	Combinational circuit	Full adder
08-12-2012	24	Virendra Tiwari	II	Combinational circuit	Flip-flops, RS flip-flop
11-12-2012	30	Virendra Tiwari	I	Arrays	Multi Dimensional Arrays
12-12-2012				Class Teaching	
13-12-2012	31	Virendra Tiwari	I	Pointers	Concepts of pointers
14-12-2012	16	Virendra Tiwari	III	Processor management	Process Scheduling

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15-12-2012	32	Virendra Tiwari	I	Pointers	Pointer operators, pointer expression
17-12-2012	25	Virendra Tiwari	II	Combinational circuit	D-Elip-Flops, JK & flip-flop
18-12-2012	26	Virendra Tiwari	II	Combinational circuit	T-flip-flop, Edge-triggered flip-flop
19-12-2012				Class Teaching	
20-12-2012	33	Virendra Tiwari	I	Pointers	Strings & string library functions
21-12-2012	17	Virendra Tiwari	III	Processor management	Job and process synchronisation
22-12-2012	34	Virendra Tiwari	I	Structures & Unions	Overview of structure of Unions
24-12-2012	35	Virendra Tiwari	I	Structure & Unions	Structure & function array of structure,
					structure & pointer nested
26-12-2012	27	Virendra Tiwari	II	Combinational circuit	Encoders
27-12-2012				Class Teaching	
28-12-2012	28	Virendra Tiwari	II	Combinational circuit	Decoders
29-12-2012	18	Virendra Tiwari	III	Processor management	Race condition
31-12-2012	36	Virendra Tiwari	I	File management	Defining & opening a file
01-01-2013	37	Virendra Tiwari	I	File management	Closing a file

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02-01-2013	19	Virendra Tiwari	III	Processor management	Deadlock detection
03-01-2013				Class Teaching	
04-01-2013	29	Virendra Tiwari	II	Combinational circuit	Multilexers
07-01-2013	30	Virendra Tiwari	II	Combinational circuit	Registers, Shift Register
08-01-2013	38	Virendra Tiwari	I	File management	Input operations
09-01-2013	39	Virendra Tiwari	I	File management	Output operations
10-01-2013	20	Virendra Tiwari	III	Processor management	Recovery
11-01-2013				Class Teaching	
12-01-2013	21	Virendra Tiwari	III	Processor management	Prevention
16-01-2013	31	Virendra Tiwari	II	Combinational circuit	Buffer Register Counters
17-01-2013	32	Virendra Tiwari	II	Basic computer organization	Instruction code
18-01-2013	22	Virendra Tiwari	III	Device management	Introduction
19-01-2013	25	Virendra Tiwari	III	Device management	Different techniques
21-01-2013				Class Teaching	
22-01-2013	24	Virendra Tiwari	III	Device management	Input-Output device

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23-01-2013	25	Virendra Tiwari	III	Device management	Storage device
24-01-2013	33	Virendra Tiwari	II	Basic computer organization	Direct & Indirect address
25-01-2013	34	Virendra Tiwari	II	Basic computer organization	Timing & control signals
28-01-2013	26	Virendra Tiwari	III	Device management	Secondary storage devices
29-01-2013				Class Teaching	
30-01-2013	27	Virendra Tiwari	III	Information management	Introduction
31-01-2013	28	Virendra Tiwari	III	Information management	Files systems
*	29	Virendra Tiwari	III	Information management	Features of file system
*	35	Virendra Tiwari	II	Basic computer organization	Instruction cycle, I/O instruction
*	36	Virendra Tiwari	II	Basic computer organization	Memory reference introduction
*	30	Virendra Tiwari	III	Information management	General model of file
*	31	Virendra Tiwari	III	Information management	Logical & physical file system
*	33	Virendra Tiwari	III	Information management	Case study of DOS
*	34	Virendra Tiwari	III	Information management	Case study of windows O.S.
*	37	Virendra Tiwari	II	Memory	Memory hierarchy, Memory write ability &

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					storage performance
*	38	Virendra Tiwari	II	Memory	Memory unit, ROM, EPROM, EEPROM,
					RAM, Flash memory
*	39	Virendra Tiwari	II	Memory	S RAM, DRAM
*	40	Virendra Tiwari	II	Memory	PSRAM, Cache Memory, Virtual Memory,
					Concept
*	41	Virendra Tiwari	II	Transistor as a switch	Logic Circuits, active & Passive
*	42	Virendra Tiwari	II	Transistor as a switch	RTL, IZZ, TTL
*	43	Virendra Tiwari	II	Transistor as a switch	ECIL, CMOS Logic inter
*	44	Virendra Tiwari	II	Multibrators	Astable multiribrators
*	45	Virendra Tiwari	II	Multibrators	Monostable multivirators Bistable multivibrators
*	46	Virendra Tiwari	II	AID converts	Digital to malog converters
*	47	Virendra Tiwari	II	AID converts	LED and LCD display
*	48	Virendra Tiwari	II	AID converts	Seven segment display
				* 19 Classes Extra	

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