

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१४-२०१५		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
02.08.2014	1	Virendra Tiwari	I	Introduction	Definition of Computer & about syllabus
04.08.2014	2	Virendra Tiwari	I	Introduction	Characteristic, evaluation
05.08.2014	1	Virendra Tiwari	II	Digital circuits	Logic gates (Introduction) & about syllabus
06.08.2014	2	Virendra Tiwari	II	Digital circuits	AND, OR Logic gates
07.08.2014	1	Sanjay Kumar Tiwari	III	Operating System	About syllabus
08.08.2014				Class Teaching	
09.08.2014	3	Virendra Tiwari	I	Introduction	Generation of computer
11.08.2014	4	Virendra Tiwari	I	Introduction	Types of computer
12.08.2014	3	Virendra Tiwari	II	Digital circuits	NAND, NOR Logic gate
13.08.2014	4	Virendra Tiwari	II	Digital circuits	Exclusive AND & XOR gates
14.08.2014	2	Sanjay Kumar Tiwari	III	Operating System overview	Computer System structure
16.08.2014				Class Teaching	
19.08.2014	5	Virendra Tiwari	I	Introduction	Major component of digital computer
20.08.2014	6	Virendra Tiwari	I	Introduction	Hardware
21.08.2014	5	Virendra Tiwari	II	Digital circuits	Universal Gates
22.08.2014	6	Virendra Tiwari	II	Digital circuits	Gates problems
23.08.2014	3	Sanjay Kumar Tiwari	III	Operating System overview	OS Structure
25.08.2014				Class Teaching	
26.08.2014	7	Virendra Tiwari	I	Introduction	Software, Firmware
27.08.2014	8	Virendra Tiwari	I	Introduction	Computer, Application
28.08.2014	7	Virendra Tiwari	II	Digital circuits	Introduction Boolean algebra
29.08.2014	8	Virendra Tiwari	II	Digital circuits	Example explanation of boolean algebra
30.08.2014	4	Sanjay Kumar Tiwari	III	Operating system overview	OS functioned, facilities
01.09.2014				Class Teaching	
02.09.2014	9	Virendra Tiwari	I	Data representation	Introduction
03.09.2014	10	Virendra Tiwari	I	Data representation	Decimal no. system, Binary no. system
04.09.2014	9	Virendra Tiwari	II	Digital circuits	Demorgham Law in Boolean algebra
05.09.2014	10	Virendra Tiwari	II	Digital circuits	Introduction k-map
06.09.2014	5	Sanjay Kumar Tiwari	III	Memory management	Introduction
10.09.2013				Class Teaching	

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१४-२०१५		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
12.09.2014	11	Virendra Tiwari	I	Data Representation	Decimal conversion to Binary conversion
13.09.2014	12	Virendra Tiwari	I	Data Representation	Octal number system
16.09.2014	11	Virendra Tiwari	II	Digital circuits	Two variable K-map
18.09.2014	12	Virendra Tiwari	II	Digital circuits	Example of two variable k.map
19.09.2014	6	Sanjay Kumar Tiwari	III	Memory management	Single Contiguous allocations
20.09.2014				Class Teaching	
22.09.2014	13	Virendra Tiwari	I	Data Representation	Hexadecimal no. system
24.09.2014	14	Virendra Tiwari	I	Data Representation	BCD number
26.09.2014	13	Virendra Tiwari	II	Digital circuits	Three variable k-map
27.09.2014	14	Virendra Tiwari	II	Digital circuits	Four variable k-map & don't care condition
29.09.2014	7	Sanjay Kumar Tiwari	III	Memory management	Partitioned allocation
30.09.2014				Monthly Evaluation	
01.10.2014	15	Virendra Tiwari	I	Data Representation	Binary arithmetic
07.10.2014	16	Virendra Tiwari	I	Data Representation	Complement, r & (r-1)'s complements
09.10.2014	15	Virendra Tiwari	II	Combinational circuits	Introduction
10.10.2014	16	Virendra Tiwari	II	Combinational circuits	Half adder
11.10.2014	8	Sanjay Kumar Tiwari	III	Memory management	Relocation partitioned allocation
13.10.2014				Class Teaching	
14.10.2014	17	Virendra Tiwari	I	Data Representation	Fixed point & floating point representation
15.10.2014	18	Virendra Tiwari	I	Introduction to programming	Concept of algorithm
16.10.2014	17	Virendra Tiwari	II	Combinational circuits	Full adder
17.10.2014	18	Virendra Tiwari	II	Combinational circuits	Introduction of flip-flops
18.10.2014	9	Sanjay Kumar Tiwari	III	Memory management	Introduction paging
27.10.2014				Class Teaching	
28.10.2014	19	Virendra Tiwari	I	Introduction to programming	Flowchart, dataflow diagram
30.10.2014	20	Virendra Tiwari	I	Programming in C	An overview of C
31.10.2014	19	Virendra Tiwari	II	Combinational circuit	RS-Flip-Flop
01.11.2014	20	Virendra Tiwari	II	Combinational circuit	D-Flip-Flop
07.11.2014	10	Sanjay Kumar Tiwari	III	Memory management	Example or problems on paging
08.11.2014				Class Teaching	

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१४-२०१५		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
10.11.2014	21	Virendra Tiwari	I	Programming in C	Structure of C program
11.11.2014	22	Virendra Tiwari	I	Programming in C	Storage class specifier
12.11.2014	21	Virendra Tiwari	II	Combinational circuit	J.K. flip-flop
13.11.2014	22	Virendra Tiwari	II	Combinational circuit	T- flip-flop
14.11.2014	11	Sanjay Kumar Tiwari	III	Memory management	Segmentation, demand page allocation
15.11.2014				Class Teaching	
17.11.2014	23	Virendra Tiwari	I	Programming in C	Data types, variable declaration
18.11.2014	24	Virendra Tiwari	I	Programming in C	Operator & Expressions
19.11.2014	23	Virendra Tiwari	II	Combinational circuit	Edge-triggered flip-flop
20.11.2014	24	Virendra Tiwari	II	Combinational circuit	Encoders
21.11.2014	12	Sanjay Kumar Tiwari	III	Processor management	State models
22.11.2014				Monthly Evaluation	
25.11.2014	25	Virendra Tiwari	I	Control Statement	True & false in C
26.11.2014	26	Virendra Tiwari	I	Control Statement	Conditional statement, if
27.11.2014	25	Virendra Tiwari	II	Combinational circuits	Decoders
28.11.2014	26	Virendra Tiwari	II	Combinational circuits	Multiplexers
29.11.2014	13	Sanjay Kumar Tiwari	III	Processor management	Job schedulers and its type
01.12.2014				Class Teaching	
02.12.2014	27	Virendra Tiwari	I	Control statements	For statement, while statement
03.12.2014	28	Virendra Tiwari	I	Control statements	do-while & break
06.12.2014	27	Virendra Tiwari	II	Combinational circuits	Introduction Register
08.12.2014	28	Virendra Tiwari	II	Combinational circuits	Shift Register
09.12.2014	14	Sanjay Kumar Tiwari	III	Processor Management	Process Scheduling
11.12.2014				Class Teaching	
12.12.2014	29	Virendra Tiwari	I	Control Statement	Continue exit () & goto
15.12.2014	30	Virendra Tiwari	I	Basic I/O	Formatted & Unformatted I/O
16.12.2014	29	Virendra Tiwari	II	Combinational circuits	Buffer register counters
17.12.2014	30	Virendra Tiwari	II	Combinational circuits	Ripple counters
18.12.2014	15	Sanjay Kumar Tiwari	III	Processor management	Job and process synchronisation
19.12.2014				Class Teaching	

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१४-२०१५		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
20.12.2014	31	Virendra Tiwari	I	Basic I/O	Concept of function
22.12.2014	32	Virendra Tiwari	I	Basic I/O	Examples of different types of functions
23.12.2014	31	Virendra Tiwari	II	Basic computer organisation	Instruction code
24.12.2014	32	Virendra Tiwari	II	Basic computer organisation	Direct & indirect address
26.12.2014	16	Sanjay Kumar Tiwari	III	Processor Management	Race conditions
27.12.2014				Monthly Evaluation	
29.12.2014	33	Virendra Tiwari	I	Basic I/O	Recursion
30.12.2014	34	Virendra Tiwari	I	Basic I/O	Parameter passing technique
31.12.2014	33	Virendra Tiwari	II	Basic computer organisation	Timing & control signals
01.01.2015	34	Virendra Tiwari	II	Basic computer organisation	Instruction Cycle
02.01.2015	17	Sanjay Kumar Tiwari	III	Processor management	Scheduling algorithm & its related problems
03.01.2015				Class Teaching	
06.01.2015	35	Virendra Tiwari	I	Array	Introduction
07.01.2015	36	Virendra Tiwari	I	Array	One dimensional
08.01.2015	35	Virendra Tiwari	II	Basic computer organisation	Memory reference instruction
09.01.2015	36	Virendra Tiwari	II	Basic computer organisation	I/O Instruction
10.01.2015	18	Sanjay Kumar Tiwari	III	Processor management	Introduction dead lock, Dead lock detection
11.01.2015				Class Teaching	
13.01.2015	37	Virendra Tiwari	I	Array	Two dimensional, multi dimensional array
17.01.2015	38	Virendra Tiwari	I	Pointers	Concept of pointers, Pointer operators
19.01.2015	37	Virendra Tiwari	II	Memory	Memory hierachy
20.01.2015	38	Virendra Tiwari	II	Memory	Memory write a ability & storage performance
21.01.2015	19	Sanjay Kumar Tiwari	III	Processor management	Recovery & prevention
22.01.2015				Class Teaching	
23.01.2015	39	Virendra Tiwari	I	Pointer	Pointer exmpressions, pointer & array
27.01.2015	40	Virendra Tiwari	I	Pointer	Strings & string library function
28.01.2015	39	Virendra Tiwari	II	Memory	Memory units
29.01.2015	40	Virendra Tiwari	II	Memory	Memory type-ROM, Masked programmed ROM
30.01.2015	20	Sanjay Kumar Tiwari	III	Device management	Introduction & its different technique
31.01.2015				Monthly Evaluation	

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-एक			पाठ्यक्रम योजना : सत्र २०१४-२०१५		विषय : कम्प्यूटर साइंस
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
02.02.2015	41	Virendra Tiwari	I	Structure of unions	Concept of structure & unions
03.02.2015	42	Virendra Tiwari	I	Structure of unions	Structure & function array structure, structure pointer, Nested structure
04.02.2015	41	Virendra Tiwari	II	Memory	EPROM, EEPROM, flash memory
05.02.2015	42	Virendra Tiwari	II	Memory	RAM, SRAM, DRAM
06.02.2015	21	Sanjay Kumar Tiwari	III	Device management	I/O device, storage devices, logical & physical, file system
07.02.2015				Class Teaching	
09.02.2015	43	Virendra Tiwari	I	File management	Defining & opening of a file
10.02.2015	44	Virendra Tiwari	I	File management	Closing a file
11.02.2015	43	Virendra Tiwari	II	Memory	Cache memory, virtual memory concept
12.02.2015	44	Virendra Tiwari	II	Memory	Synchronous & Enhanced Synchronous DRAM
13.02.2015	22	Sanjay Kumar Tiwari	III	Information management	File system, case study of DOS & window operating system