

महाराणा प्रताप पी.जी. कालेज, जंगल धूसड़, गोरखपुर

कक्षा : बी.एस-सी. भाग-तीन पाठ्यक्रम योजना : सत्र-2019-20 विषय : कम्प्यूटर साइंस					
दिनांक	व्याख्यान	प्राध्यापक का नाम	प्रश्नपत्र	अध्याय	शीर्षक
16.07.19	1	Virendra Tiwari	I	Principle of comp. design	Basic components of computer system
17.07.19	2	Virendra Tiwari	I	Principle of Comp.design	Overview of Construction of ALU
18.07.19	1	Harvind Kr. Srivastav	III	Object Oriented System	Introduction to object oriented System
19.07.19	2	H.K.S	III	Object Oriented System	Overview of object oriented System
20.07.19	1	Virendra Tiwari	II	DBMS	Concept of Database and its evaluation
22.07.19	1	H.K.S	IV	Data Communication System	Transmission System
22.07.19	-		-	CLASS TEACHING	
23.07.19	2	H.K.S	IV	Data Communication System	Purpose, Components Source Transmitter
23.07.19	3	Virendra Tiwari	I	Principle Comp. Design	Overview of Operation of ALU
24.07.19	4	Virendra Tiwari	I	Register Transfer & Micro Operator	Register Transfer Language
25.07.19	3	H.K.S	III	OOPS	Abstract data types
26.07.19	4	H.K.S	III	OOPS	Inheritance
27.07.19	2	Virendra Tiwari	II	DBMS	Data Abstraction
29.07.19	-		-	CLASS TEACHING	
30.07.19	3	H.K.S	IV	Data Communication System	Data Transmission: Frequency
30.07.19	5	Virendra Tiwari	I	Register Transfer & micro-Operator	Bus & Memory Transfer
31.07.19	6	Virendra Tiwari	I	Register Transfer & micro-Operator	Logic micro operators
01.08.19	5	H.K.S	III	OOPS	Polymorphism
02.08.19	6	H.K.S	III	OOPS	Object Identity
03.08.19	3	Virendra Tiwari	II	DBMS	Data integration
06.08.19	4	H.K.S	IV	Data Communication System	Spectrum & Band Width
06.08.19	-		-	CLASS TEACHING	
07.08.19	7	Virendra Tiwari	I	Register Transfer & micro-operator	Shift micro operators
08.08.19	8	Virendra Tiwari	I	Register Transfer & micro-operator	Bus Structure, Multiple bus Hierarchies
09.08.19	7	H.K.S	III	OOPS	Object modling concepts
10.08.19	8	H.K.S	III	OOPS	Object oriented programing language
13.08.19	5	H.K.S	IV	Data Communication System	Analog Data Tansmission
13.08.19	4	Virendra Tiwari	II	DBMS	Data Models, Components of DBMS
14.08.19	-		-	CLASS TEACHING	
16.08.19	9	Virendra Tiwari	I	Register Transfer & micro-operator	Elements of Bus Design

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17.08.19	10	Virendra Tiwari	I	Register Transfer & Micro-Operator	PCI Bus Structure
19.08.19	6	H.K.S	IV	Data Communication System	Digital Data Transmission
19.08.19	9	H.K.S	III	OOPS	Object oriented programing
20.08.19	7	H.K.S	IV	Data Communication System	Data & Signals
20.08.19	10	H.K.S	III	OOPS	Overview of C++ Programing
21.08.19	5	Virendra Tiwari	II	DBMS	Three level of Architecture of DBMS
22.08.19	-		-	CLASS TEACHING	
24.08.19	11	Virendra Tiwari	I	Memory	Linear Structure Memory Organisation & Coders
26.08.19	8	H.K.S	IV	Data & Signal	Analog & Digital Signaling of analog & Digital data
26.08.19	12	Virendra Tiwari	I	Memory`	Dimension of Memory Access
27.08.19	9	H.K.S	IV	Data & Signal	Modem
27.08.19	11	H.K.S	III	C++ Programming	Support for data Obstraction
28.08.19	12	H.K.S	III	OOS (C++)	Support for object oriented programing
29.08.19	6	Virendra Tiwari	II	DBMS	Data Models Implementation
30.08.19	-		-	CLASS TEACHING/TEST	
02.09.19	10	H.K.S	IV	Data & Signal	Modulation Techniques
02.09.19	13	Virendra Tiwari	I	Memory	Connectivity memory chip to a computer trees
03.09.19	11	H.K.S	IV	Data & Signal	CODEC, Digital Transmitter
03.09.19	14	Virendra Tiwari	I	Memory	Magnetic Disk, Floppy Disk
04.09.19	13	H.K.S	III	C++ Programming Langauage	Declaration & Constatnts
05.09.19	14	H.K.S	III	C++ Programming Langauage	Function & File
06.09.19	7	Virendra Tiwari	II	DBMS	Relation Data
07.09.19	-		-	CLASS TEACHING	
09.09.19	12	H.K.S	IV	Data & signal	Transmission Impairments
09.09.19	15	Virendra Tiwari	I	Memory	Magnetic Tape
11.09.19	16	Virendra Tiwari	I	I/O Device	Cassettes & Cartidge
13.09.19	15	H.K.S	III	C++ Programming Langauage	Linkages
14.09.19	16	H.K.S	III	Class & Objects	Defination of class
16.09.19	13	H.K.S	IV	Data & signal	Attenuation & Attenuation distrortion
16.09.19	8	H.K.S	II	DBMS	Network Hierarchical

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17.09.19	14	H.K.S	IV	Data & Signal	Delay Distortion, noise
17.09.19	-			CLASS TEACHING	
19.09.19	17	Virendra Tiwari	I	I/O Device	Fundamentals of Keyboards, Printers
20.09.19	18	Virendra Tiwari	I	I/O Device	CRT & LCD Displays Devices
21.09.19	17	H.K.S	III	Class & Objects	Class Declaration
23.09.19	15	H.K.S	IV	Data & Signal	Introduction to Network
23.09.19	18	H.K.S	III	Class & Objects	Object Declaration
24.09.19	16	Virendra Tiwari	IV	Data & Signal	TCP/IP Reference Method
24.09.19	9	Virendra Tiwari	II	DBMS	Relation & Manipulation
25.09.19	-		-	CLASS TEACHING/TEST	
26.09.19	19	Virendra Tiwari	I	CPU	General Register & Organisation
27.09.19	20	Virendra Tiwari	I	CPU	Memory Stack
30.09.19	17	H.K.S	IV	Transmission Media	Magnetic Media
30.09.19	19	H.K.S	III	Class & Objects	Class Function Definition Member
01.10.19	20	H.K.S	III	Class & Objects	Definition Outside the class, declaration,
					scope resolution operator
03.10.19	10	Virendra Tiwari	II	DBMS	Relational Algebra
04.10.19	-		-	CLASS TEACHING	
05.10.19	21	Virendra Tiwari	I	CPU	One Address & Two Address Instructions
12.10.19	22	Virendra Tiwari	I	CPU	Data Transfer
14.10.19	18	H.K.S	IV	Transmission Media	Twisted Pair Cable
14.10.19	21	H.K.S	III	Class & Object	Private & Public member Functions
15.10.19	19	H.K.S	IV	Transmission Media	Baseband & Broadband Coaxial Cables
15.10.19	22	H.K.S	III	Class & Object	Nesting member function creating objects
16.10.19	11	Virendra Tiwari	II	DBMS	Relational Calculus SQL
17.10.19				CLASS TEACHING	
18.10.19	23	Virendra Tiwari	I	CPU	Arithmetic Logic & Shift
21.10.19	20	H.K.S	IV	Transmission Media	Fiber Optics
21.10.19	24	Virendra Tiwari	I	CPU	Software & Hardware Interrupt
22.10.19	21	H.K.S	IV	Wireless Transmission	Radio Transmission

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22.10.19	23	H.K.S	III	Calsses & Objects	Accesssing Member Functions
23.10.19	24	H.K.S	III	Calsses & Objects	Accesssing Member Functions
30.10.19	12	Virendra Tiwari	II	DBMS	Funtional Dependencies Ist & 3rd
31.10.19	-		-	CLASS TEACHING/TEST	Normal form
01.11.19	25	Virendra Tiwari	I	CPU	Arithmetic & Instruction Pipr
04.11.19	22	H.K.S	IV	Wireless Transmission	Radio Transmission
04.11.19	26	Virendra Tiwari	I	Control Unit	Construction of instruction
05.11.19	23	H.K.S	IV	Wireless Transmission	Microwave Transmission
05.11.19	25	Virendra Tiwari	II	Class & Objects	Array of Objects
06.11.19	26	Virendra Tiwari	II	Class & Objects	Objects as function agreement
07.11.19	13	Virendra Tiwari	II	Relation Database Design	BCNF
09.11.19	-			CLASS TEACHING	
11.11.19	24	H.K.S	IV	Wireless Transmission	ISDN, ATM
11.11.19	27	Virendra Tiwari	I	Control Unit	Intruccion Cycle Organisation
13.11.19	28	Virendra Tiwari	I	Control Unit	Control Register
14.11.19	27	H.K.S	III	Operator Overloading	Opertaor Function, User Defined
					Type conversion
15.11.19	28	H.K.S	III	Operator Overloading	Literal , Large Object
16.11.19	14	Virendra Tiwari	II	FoxPro	Introduction of FoxPro
18.11.19	25	H.K.S	IV	Wireless Transmission	Data Linklayer, Services, Framing
18.11.19	-			CLASS TEACHING	
19.11.19	26	H.K.S	IV	Wireless Transmission	Error control, error detecting & correcting
					codes
19.11.19	29	Virendra Tiwari	I	Control unit	Execution cycle organisation of control
					registers
20.11.19	30	Virendra Tiwari	I	Control unit	Sequence of operation of control register
21.11.19	29	H.K.S	III	Operator Overloading	Assignment & Imitialisation Subscriping
22.11.19	30	H.K.S	III	Operator Overloading	A String Class
23.11.19	15	Virendra Tiwari	II	FoxPro	Database Construction

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25.11.19	27	H.K.S	IV	Wireless Transmission	Data links protocols
25.11.19	-			CLASS TEACHING	
26.11.19	30	H.K.S	IV	HDLC	Static & dynamic channel allocation in Lans
27.11.19	32	Virenda Tiwari	I	Control unit	Branch
28.11.19	32	H.K.S	III	Operator overloading	Inheritance through extending classes
29.11.19	32	H.K.S	III	Operator overloading	Inheritance through extending classes
30.11.19	17	Virendra Tiwari	II	Foxpro	Searching
02.11.19	30	H.K.S	IV	HDLC	Dynamic channel allocation in Lans
02.11.19	-			CLASS TEACHING/TEST	
03.11.19	31	H.K.S	IV	Multiple Access Protocol	ALOHA, Congestion Control algo
03.11.19	33	Virendr Tiwari	I	Control Unit	Arithmetic operators
07.11.19	34	Virendr Tiwari	I	Control Unit	Skip or Jump Instruction
09.11.19	32	H.K.S	IV	Multiple access protocol	CSMA/CD, IEEE Standards 802.3
09.11.19	34	H.K.S	III	Inheritance through extending classes	Visibility mode, Signal Inheritance
11.11.19	33	H.K.S	III	Inheritance through extending classes	Derived Class, defining derived classes
12.12.19	17	Virendra Tiwari	II	Foxpro	Sorting
13.12.19	-			CLASS TEACHING	
14.12.19	35	Virendra Tiwari	I	Control Unit	Register Language
16.12.19	33	H.K.S	IV	MAP	Ethernet, 802.4, Token Bus
16.12.19	36	Virendra Tiwari	I	Control Unit	Micro-Programming
17.12.19	35	H.K.S	III	Streaming & Templates & Design for Libral	C-I/O Formatting Files & Streams
18.12.19	36	H.K.S	III	Steam Temples	C I/O Design of Liberties
19.12.19	18	Virendra Tiwari	II	DBMS	Indexing
20.12.19	-			CLASS TEACHING	
21.12.19	37	Virendra Tiwari	I	Control Unit	Micro-Programming
23.12.19	4	H.K.S	IV	MAP	Ethernet, 802.4, Token ring, Bridges
23.12.19	38	Virendra Tiwari	I	Control Unit	Micro-Programming
24.12.19	35	H.K.S	IV	MAP	Routers, Gateways Routing algos
24.12.19	37	H.K.S	III	OOA & Design	Object Oriented Analysis System Design
26.12.19	38	H.K.S	III	OOA & Design	Object Design

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27.12.19	19	Virendra Tiwari	II	Foxpro	Updation
28.12.19	-			CLASS TEACHING	
30.12.19	38	H.K.S	IV	MAP	Internet Working
30.12.19	39	Virendra Tiwari	I	Control Unit	Program
31.12.19	38	H.K.S	IV	MAP	IP Addressing
31.12.19	40	Virendra Tiwari	I	Control Unit	Micro-Programming
01.01.20	39	H.K.S	III	OOA & Design	Semantic
02.01.20	40	Virendra Tiwari	III	OOA & Design	Entity, Relationship
03.01.20	20	Virendra Tiwari	II	Foxpro	Data manage
04.01.20	-			CLASS TEACHING	
06.01.20	39	H.K.S	IV	MAP	IP Addressing numericab
06.01.20	41	Virendra Tiwari	I	Control Unit	Shift Instruction
07.01.20	40	H.K.S	IV	MAP	Revision
07.01.20	42	Virendra Tiwari	I	Control Unit	Revision
08.01.20	41	H.K.S	III	OOA & Design	Modeling
09.01.20	42	H.K.S	III	OOA & Design	Booch's
10.01.20	22	Virendra Tiwari	II	FoxPro	Screen, Designing, Programming
11.01.20	-			CLASS TEACHING/TEST	
13.01.20	41	H.K.S	IV	MAP	Problems
13.01.20	43	Virendra Tiwari	I	Control Unit	Revision
16.01.20	44	Virendra Tiwari	I	Principal of Computer Design	Revision
17.12.20	43	H.K.S	III	OOA & Design	Booch's ooa, ood approach
18.12.20	43	H.K.S	III	OOA & Design	Sementic Rules
20.12.20	43	H.K.S	IV	MAP	Revision
20.12.20	23	Virendra Tiwari	II	DBMS	Revision
22.12.20	-			CLASS TEACHING	
23.01.20	45	Virendra Tiwari	I	Principal of Computer Design	Revision
24.01.20	45	Virendra Tiwari	I	Principal of Computer Design	Revision
27.01.20	43	H.K.S	IV	MAP	Revision
27.01.20	44	H.K.S	III	OOA & Design	Overview of Existing Methodlogy

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